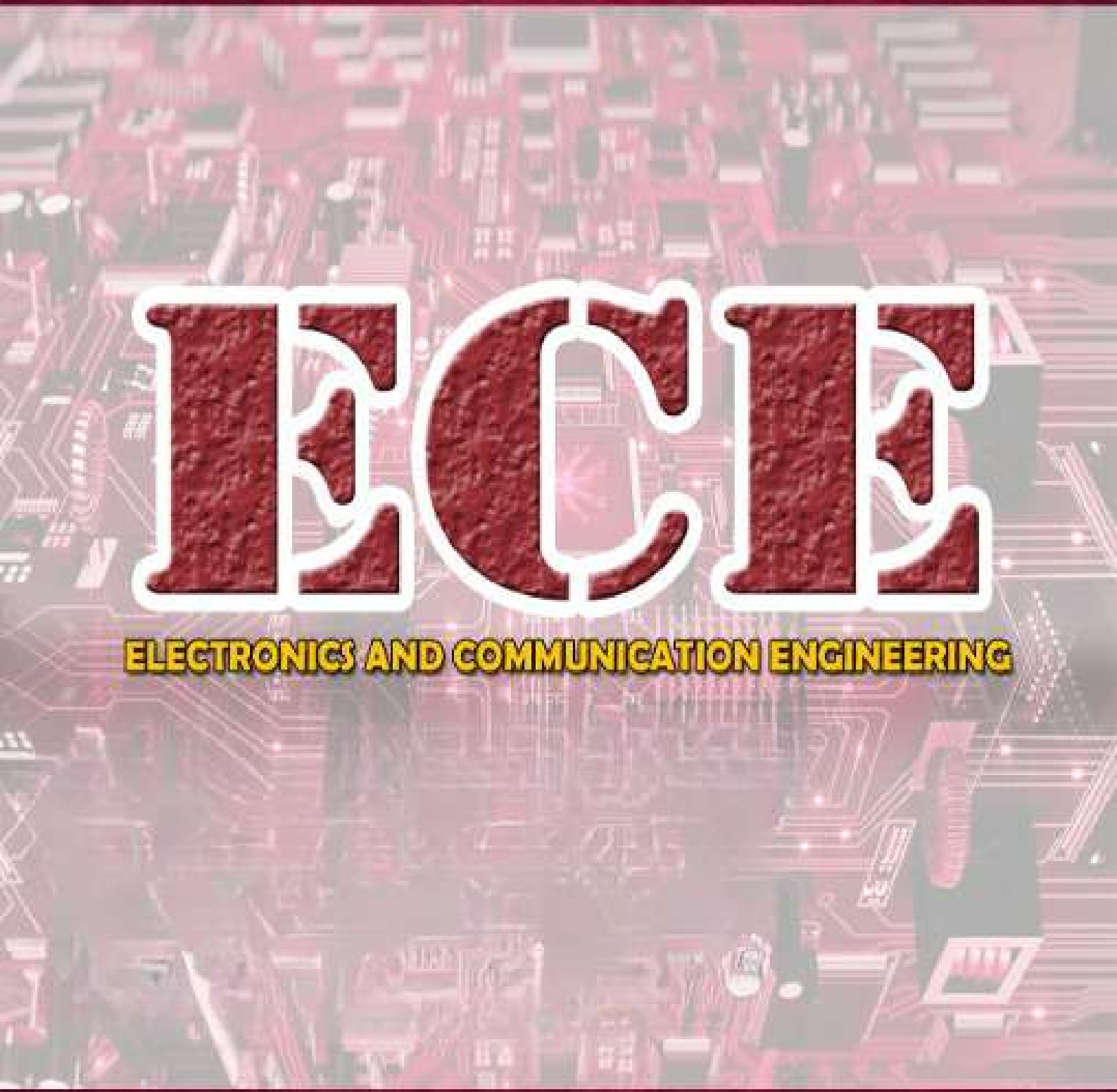


NEWSLETTER

**VOLUME 1 ISSUE 1
DECEMBER, 2020**

The background of the entire page is a detailed, high-resolution image of a printed circuit board (PCB). It shows a complex network of copper traces, various electronic components like resistors, capacitors, and integrated circuits, and a dense layout of solder points. The colors are primarily metallic (copper, silver) with some green from the board's substrate. Overlaid on this background is the acronym 'ECE' in a very large, bold, serif font. The letters are filled with a dark red color and have a thick white outline, making them stand out prominently in the center of the page.

ECE

ELECTRONICS AND COMMUNICATION ENGINEERING

ADITYA COLLEGE OF ENGINEERING & TECHNOLOGY

Vision

-->To emerge as a centre of excellence in education and Research

Mission

-->To establish skill and learning centric infrastructure in thrust areas

-->To develop Robotics and IOT based infrastructure Laboratories

-->To organize events through industry institute collaborations and promote innovation

-->To disseminate knowledge through quality teaching learning process.



Dr. R V V KRISHNA
HOD ECE

ECE department was established in the year 2004 with an intake of 60 students and now it has been expanded with an intake of 240 students. ECE plays a vital role in Technology Revolution. Our main aim is to generate new knowledge by engaging in cutting-in research to promote academic growth and to develop human potential to its fullest extent so that intellectually capable & imaginatively gifted leaders can emerge in a range of professions. We have Modern state of the art and well furnished labs like Microwave and Optical Communication Lab, Electronic Devices and Circuits lab, Modern Communication Lab, Research lab etc with excellent laboratory facilities and dedicated faculty.

PLACEMENT POINT



21



9



40



12



14



2

TOPPERS (2019 – 20)

IV B.TECH - II SEM



K. RAMA DEVI
16P31A0485
SGPA 9.25



N. KEERTHIKA
16P31A04L4
SGPA 9.25



SUSHMITA PRIYA
16P31A04H2
SGPA 9.13



K. SAI NIHARIKA
16P31A04M4
SGPA 9.13

PATENT PENDING



Dr. B V VIJAYASRI
ASSOCIATE PROF

- 1) A Novel Autonomic Architecture to Assure Security in Cloud Environments**
-



Dr. T S KARTHIK
PROFESSOR

- 1) Development of Iot Based Underground Wire Fault Detection System**
- 2) Design and Analysis Of Self-Protection Framework System Integrate With Fog Computing and IOT**

PUBLICATIONS

1. A. Arun Kumar Gudivada," Novel optimized tree-based stack-type architecture for 2n-bit comparator at nanoscale with energy dissipation analysis", The Journal of Supercomputing, Vol 77, Oct 2020, ISSN 1573-0484, <https://link.springer.com/article/10.1007/s11227-020-03453-1>
2. R. Anil Kumar," Performance Analysis of GFDM Modulation in Heterogeneous Network for 5G NR", Wireless Personal Communications, Vol 116, Sept 2020, ISSN 0929-6212, <https://doi.org/10.1007/s11277-020-07791-4>
3. P. Anantha Sravanthi," Road extraction using aerial images for future navigation", Materials Today: Proceedings, Vol 47, June, 2020, ISSN 2214-7853, doi.org/10.1016/j.matpr.2021.05.537
4. P. Ramesh Kumar," NSCT And Eigen Features Based Image Fusion", Solid State Technology, Volume 63, Issue 5, 2020, ISSN 0038-111X, <https://solidstatetechnology.us/index.php/JSST/article/view/5907>
5. A. Arun Kumar Gudivada," A Systematic review on full adder designs in Quantum dot cellular automata", Materials Today Proceedings, Vol 45, Aug 2020, ISSN 2214-7853, doi.org/10.1016/j.matpr.2020.08.475
6. Sneha M Joseph," Physical Unclonable Function: A Succinct Study", ADALYA JOURNAL, September 2020, ISSN NO: 1301-2746, <https://doi.org/10.37896/aj9.9/011>
7. K. Parvateesam , " Design and Implementation of Wireless Blended Astute Drone Drifter with VR Technology for Smart Cities", International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE), Volume 9, October 2020, ISSN 2278 – 8875, http://www.ijareeie.com/upload/2020/october/4_KP_DRONE_NC_NEW.pdf
8. B. V. Vijayasri," Speech enhancement using modified wiener filter based MMSE and speech presence probability estimation", International Journal of Informatics and Communication Technology (IJ-ICT), Volume 9, Aug, 2020, ISSN 2252-8776, <https://zenodo.org/record/4243338#.YcqTnWhBy3A>

9. G. A. Arun Kumar, K. Jayaram Kumar, Durga Prasad Siddani" Design of area-efficient high speed 4×4 Wallace tree multiplier using quantum-dot cellular automata", Journal: Materials Today, Elsevier, Vol 45, Sept 2020, ISSN:2214-7853, doi.org/10.1016/j.matpr.2020.07.677

10. G. A. Arun Kumar,"Performance evaluation of noise coupling on Germanium based TSV filled material for future IC integration technique", Journal: Materials Today, Elsevier, Vol 45, Sept 2020, ISSN:2214-7853, doi.org/10.1016/j.matpr.2020.07.631

11. A. Arun Kumar Gudivada," Review on Quantum-dot Cellular automata full adders", Journal of study of Research, Vol 12, Nov, 2020,ISSB 1781-7838





EDITORIAL BOARD

- | | |
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| 4. S D SATISH REDDY | 18P31A04M7 |
| 5. T DEEPAK REDDY | 18P31A04B1 |
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