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ADITYA COLLEGE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, Permanently Affiliated to JNTUK, KAKINADA)

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the project report entitled "A NOVEL SINGLE STAGE SINGLE PGASE RECONFIGURABLE INVERTER TOPOLOGY FOR A SOLAR POWERED HYBRID AC/DC HOME" being submitted by

P. VEERENDRA SAINATH	18P35A0232
M. NANI	18P35A0222
V. S.S. DILEEP KUMAR	18P35A0238
Y. RAJENDRA	18P35A0239

In the partial fulfillment of the requirement for the award of the degree of Bachelor of Technology in Electrical and Electronics Engineering from Aditya college of engineering and technology. It is the record of Bonafide work, carried out by them at Aditya College of Engineering and Technology

Project Guide

[Signature] 26/7/2021
Miss. K. PRABHA RANI

M.Tech

Assistant Professor

Head of the Department

[Signature]
Mr. RAJESH MURARI

M.Tech,(Ph.D)

Associate Professor

[Signature]
EXTERNAL EXAMINER

[Signature]
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Aditya College of
Engineering & Technology
SURAMPalem- 533 437


CERTIFICATE



This is to certify that project report entitled "VLSI DESIGN OF FIXED WIDTH BOOTH MULTIPLIER USING MULTILEVEL CONDITIONAL PROBABILITY" is being submitted by

N. SRI VENKATA BHASKAR	18P35A0229
K. SAI PRAKASH	18P35A0215
K. GANESH SAI REDDY	18P35A0213
S. CHANDRA SHEKHAR	18P35A0234

in the partial fulfilment of the requirements for the award of degree of the Bachelor of Technology in Electrical and Electronics Engineering. It is the record of bonafide work carried out by them under the esteemed guidance and supervision of Mr. S. Reddy Ramesh, M.Tech, Assistant Professor.


Project Guide

Mr. S. Reddy Ramesh, M.Tech

Assistant Professor


Head of the Department

Sri. Rajesh Murari, M.Tech (Ph.D)

Associate Professor

Department of Electrical and Electronics Engineering
ADITYA COLLEGE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, Affiliated to JNTUK, Kakinada)

Surampalem, ADB Road, East Godavari Dist., Andhra Pradesh

(2017-2021)


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SURAMPALEM- 533 437

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This is to certify that project report entitled "AUTONOMOUS POWER MANAGEMENT FOR INTERLINKED AC-DC MICRO GRIDS" is being submitted by

BURREY UMA NEERAJ	18P35A0249
TANAKALA NAVEEN KUMAR	18P35A0278
EMANDHI SRINIVAS	18P35A0254
KOPPIREDDI HARISH	18P35A0267
KOPPANA SAI KRISHNA	17P31A0218

in the partial fulfilment of the requirements for the award of degree of the Bachelor of Technology in Electrical and Electronics Engineering. It is the record of bona fide work carried out by them under the esteemed guidance and supervision of Miss. V. U. P. LAVANYA M.Tech., Assistant Professor.

V. U. P. Lavanya
Project Guide

Miss. V. U. P. LAVANYA M.Tech

Assistant Professor

Head of the Department

Mr. M. Rajesh Murari M. Tech., (Ph.D)

Associate Professor

ADITYA COLLEGE OF ENGINEERING & TECHNOLOGY

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SURAMPalem, GANDEPALLI MANDAL – 533295(A.P)

2017-2021

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This is to certify that the project entitled "PV SOLAR SYSTEM CONTROL AS STATCOM (PV-STATCOM) FOR POWER OSCILLATION DAMPING" that is being submitted by CH.V.V.SATHISH(18P35A0206), CH.AKHIL REDDY(18P35A0207), A.PAVAN MAHESH(18P35A0202), B.L.K.M.S.GANESH (17P31A0205) in fulfilment of the requirements for the award of degree of Bachelor of Technology in ELECTRICAL AND ELECTRONICS ENGINEERING, Aditya College of Engineering and Technology, Surampalem.

V.U.P. Lavanya
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Mrs.V.U.P.LAVANYA
M.Tech

Assistant Professor

Mr. Rajesh Murari
Head of the Department

Mr. RAJESH MURARI
M. Tech, (Ph.D.)

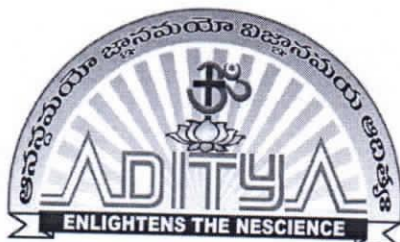
Associate Professor

[Signature]
EXTERNAL EXAMINER

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SURAMPALAM- 533 437

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the thesis entitled “**Transmission line fault detection and indication to electricity board using GSM**” that is being submitted by

MUKKALA KIRAN KUMAR	18P35A0226
VADISELA YUGANDHAR KUMAR	18P35A0237
AMALAKANTI SURESH	18P35A0201
CHAGANTI SESHU MOHAN	18P35A0205

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of Mr. M P SUBBA RAJU


Project Guide

Mr. M P SUBBA RAJU
M.Tech
Assistant Professor

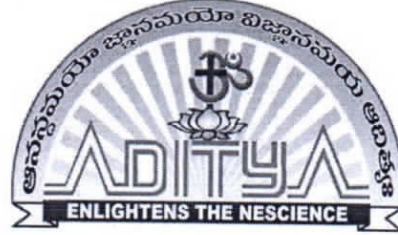

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This is to certify that the thesis entitled “A Novel Control Method For Transformerless H-Bridge Cascaded STATCOM With Star Configuration” that is being submitted by

YEDLA SAI ROHIT KUMAR

17P31A0232

RAMADEVU SIVA SANKAR

18P35A0274

POLISSETTY RAMA KRISHNA NAGA CHANDU

18P35A0272

VEERAMALLA SAMPATH SAGAR

18P35A0279

YELUGUBANTI SAIGANESH

17P31A0233

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Project Guide

Mr.M P SUBBA RAJU
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Head of the Department

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the Project entitled "Optimal Allocation of DGs and AVR's in Radial Distribution Networks Using Harmonic Search Algorithm" that is being submitted by D.A.C. Saradhi (18P35A0251), E. Anisha (18P35A0259), H. Uday Kiran (18P35A0260), Y. Pavan Teja (18P35A0280), E. Lasya (18P35A0253) in fulfilment of the requirements for the award of degree in the department of ELECTRICAL AND ELECTRONICS ENGINEERING, Aditya College of Engineering and Technology, Surampalem.


Project Guide

Mr. K.R.K.V. PRASAD
M.Tech (Ph.D)

Associate Professor


Head of the Department

Mr. RAJESH MURARI
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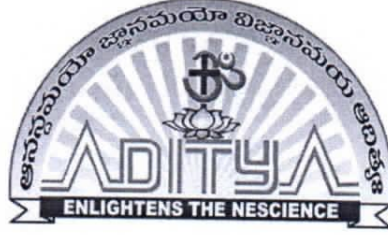
Associate Professor


EXTERNAL EXAMINER


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This is to certify that the thesis entitled “SMART HOME AUTOMATION USING IOT” that is being submitted by

PENTAKOTA SAI TEJASWINI	17P31A0224
ANUSHA	
BURELA NARENDRABABU	18P35A0247
SWETHA SHALINI	17P31A0230
ALLA MANOJ	18P35A0242

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of Sri. KURRA VENKATESWARA RAO


Project Guide

Sri. K. VENKATESWARA RAO

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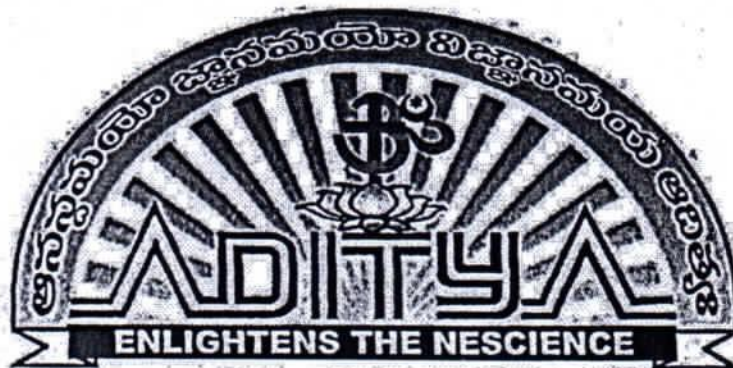
Associate Professor


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SURAMPalem- 533 437

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(Affiliated to JNTUK, Kakinada, Approved by AICTE, Accredited by NAAC)

Surampalem-533 437.



Certificate

This is to certify that the thesis/dissertation entitled "Development of ARDUNIO Based Efficient Solar Tracker Photovoltaic System" is being submitted by B. KOUSHIK BALA NAGA SAI (18P35A0203), G. SAI KUMAR (18P35A0211), K. NAGA KINNERA (17P31A0216), S. SATYA PRAKASH (18P345A0233) in partial fulfilment for the award of B. Tech in Electrical and Electronics Engineering to the Jawaharlal Nehru Technological University Kakinada is a record of Bonafide work carried out by him/her at our Organization/Institution.

Abhisekh Anand
PROJECT GUIDE

MR. ABHISEKH ANAND
M. TECH
Assistant Professor

Rajrsh Murari
HEAD OF THE DEPARTMENT

MR. RAJRSH MURARI
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(Affiliated to JNTUK, Kakinada and Approved by AICTE, New Delhi)
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING



CERTIFICATE

This is to certify that the thesis entitled "DEVELOPMENT OF A BIDIRECTIONAL DC/DC CONVERTER WITH DUAL-BATTERY ENERGY STORAGE FOR HYBRID ELECTRIC VEHICLE SYSTEM" is being submitted by

CH. VNS. JYOTHI	-	18P35A0248
O.S.S. DAKSHIN	-	17P31A0222
K. GIRIDHAR	-	18P35A0268
S. GOPAL	-	18P35A0275

in partial fulfillment of the requirements for the award of degree of B. Tech in Electrical and Electronics Engineering from Jawaharlal Nehru Technological University Kakinada is a record of bona fide work carried out by them at Aditya college of engineering and technology.

The results embodied in this Project report have not been submitted to any other University or Institute for the award of any degree or diploma.


PROJECT GUIDE

Sri. R. SRINIVAS M.TECH
Assistant Professor


HEAD OF THE DEPARTMENT

Sri. M. RAJESH M.TECH
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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING



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This is to certify that the thesis entitled "DEVELOPMENT OF A BIDIRECTIONAL DC/DC CONVERTER WITH DUAL-BATTERY ENERGY STORAGE FOR HYBRID ELECTRIC VEHICLE SYSTEM" is being submitted by

CH. VNS. JYOTHI	-	18P35A0248
O.S.S. DAKSHIN	-	17P31A0222
K. GIRIDHAR	-	18P35A0268
S. GOPAL	-	18P35A0275

in partial fulfillment of the requirements for the award of degree of B. Tech in Electrical and Electronics Engineering from Jawaharlal Nehru Technological University Kakinada is a record of bona fide work carried out by them at Aditya college of engineering and technology.

The results embodied in this Project report have not been submitted to any other University or Institute for the award of any degree or diploma.


PROJECT GUIDE

Sri. R. SRINIVAS M.TECH
Assistant Professor


HEAD OF THE DEPARTMENT

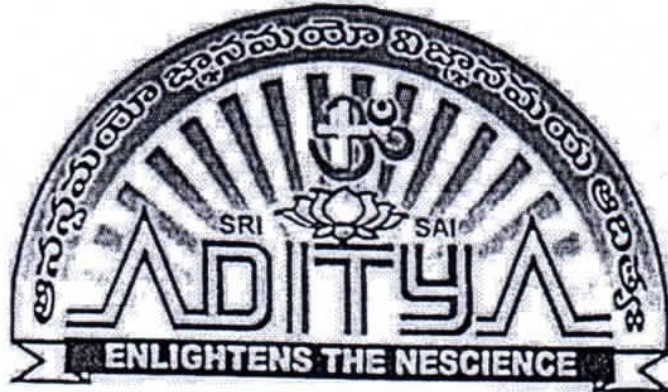
Sri. M. RAJESH M.TECH
Professor


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CERTIFICATE

ADITYA COLLEGE OF ENGINEERING AND TECHNOLOGY



This is to certify that project report entitled "MITIGATION OF VOLTAGE SAG AND SWELL USING DYNAMIC VOLTAGE RESTORER" is being submitted by

L.SHIRISHA	17P31A0219
K.J.V.S.S KUMAR	18P35A0262
M.R.KRISHNA	17P31A0221
CH.D.KALYANRAM	18P35A0250

in fulfillment of the requirement for the award of degree in the
Department of Electrical and Electronics Engineering,
ADITYA COLLEGE OF ENGINEERING AND TECHNOLOGY

Abhisekh Anand
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Mr ABHISEKH ANAND M.Tech

Assistant Professor

Rajesh Murari
Head of the Department

Mr.RAJESH MURARI M.Tech.,(Ph.D)

Associate Professor

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Surampalem, ADB road, East Godavari District, A.P-533437

CERTIFICATE



This is to certify that project report entitled "AUTOMATED COVID TEMPERATURE BASED GATE OPENING AND VISITOR COUNTER SYSTEM" is being submitted

by

G.CH.SAI MANIKANTA	18P35A0258
G. LEELA ABHIRAM	18P35A0257
S.J. VENKATA ARAVIND	18P35A0276
N. SUMAN RAJU	18P35A0270
A. VARA PRASAD	17P35A0201

in the partial fulfilment of the requirements for the award of degree of the Bachelor of Technology in Electrical and Electronics Engineering, during the academic year of 2017-2021. The results of embodied in this project report have not been submitted to any other institute or university for the award of any degree.


Project Guide

Mr.S. REDDY RAMESH M.Tech

Assistant Professor


Head of the Department

Mr. RAJESH MURARI M.Tech Ph.D

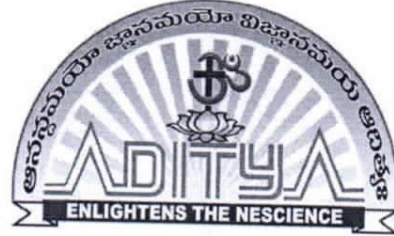
Associate Professor


EXTERNAL EXAMINER

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the thesis entitled "Grid-Connected PV-Wind-Battery based Multi-Input Transformer Coupled Bidirectional DC-DC Converter for household Applications" that is being submitted by

KALIMI SAI BABU	18P35A0212
POSINA DIVANAM	18KT5A0209
INTI NAGA SURESH	17P31A0213
KARRI TEJA	18P35A0214

in fulfilment of the requirements for the award of degree of B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING is a record of bonified work carried out by them under the guidance and supervision of Sri. KURRA VENKATESWARA RAO


Project Guide

Sri KURRA VENKATESWARA RAO
M.Tech
Assistant Professor

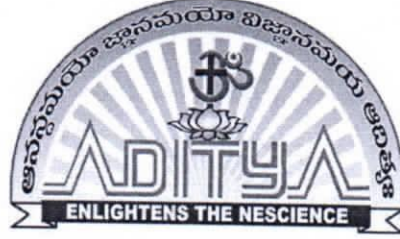

Head of the Department

Mr. RAJESH MURARI
M.Tech, (Ph.D)
Associate Professor


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SURAMPALAM- 533 437

CERTIFICATE

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the thesis entitled “**Autonomous PV-Array Excited Wind Driven Induction Generator for off grid Application**” that is being submitted by

DAMMALA NAVEENA

17P31A0207

DHONI VENKATA LAXMI NARASIMHA MURTHY

18P35A0208

EDARA VIGNESH MADHAV KUMAR

18P35A0209

DURUPUDI MANIKANTA

17P31A0209

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of Mrs. SANA VANI


Project Guide

Mrs. SANA VANI
M.Tech
Assistant Professor

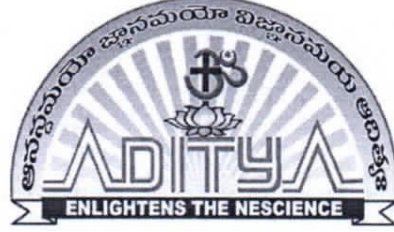

Head of the Department

Mr. RAJESH MURARI
M.Tech,(Ph.D)
Associate Professor


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Engineering & Techno!
SURAMPALEM- 533

CERTIFICATE

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the thesis entitled “**BLDC Motor Driven Solar PV Array-Fed Water Pumping Syatem Employing Zeta Converter**”isbeing submitted by

MADHAVAREDDY KRANTHI KUMAR	18P35A0269
IVVALA UTTEJ	18P35A0261
BONAM SAI SATYANARAYANA	18P35A0246
FARAZ KHAN	17P31A0234
GANTA DURGA PRASAD	18P35A0256

in fulfilment of the requirements for the award of degree of **B.Tech inELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of Mrs.SANA VANI


Project Guide

Mrs.SANA VANI
Assistant Professor

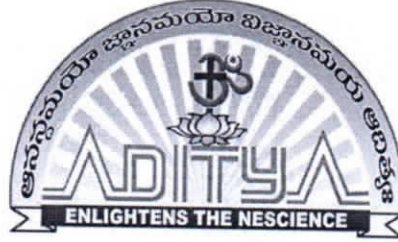

Head of the Department

Mr. RAJESH MURARI
M.Tech,(Ph.D)
Associate Professor


Aditya Cones
Engineering & Technology
SURAMPALEM- 533 437

CERTIFICATE

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the thesis entitled “**OPTIMAL ALLOCATION OF DG AND AVR IN DISTRIBUTION SYSTEM USING HAS**” that is being submitted by

KOLA SIVAKRISHNA	17P31A0215
MARNA PAVAN	18P35A0223
MEDETI NAVEEN KUMAR	18P35A0225
KIKKISETTI DURGA PRASAD	18P35A0217
YANAMALA NAGENDRA	18P35A0240


in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of **Mr.K R K V Prasad**


Project Guide

Mr.K R K V Prasad
M.Tech,(Ph.D)
Associate Professor

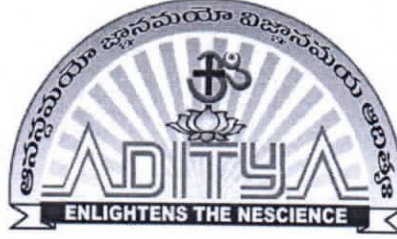

Head of the Department

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M.Tech,(Ph.D)
Associate Professor


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SURAMPALEM- 533 437

CERTIFICATE

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the thesis entitled “**Design and Performance Analysis of Three-Phase Solar PV Integrated UPQC**” that is being submitted by

PRITESH RANJAN SINGH	17P31A0226
KAMANA VINAY KUMAR	18P35A0263
SAYYED BASHEERUDDIN	17P31A0229
PIDUGU NAVEEN PREM KUMAR	17P31A0225
GANISETTI DURGA PAVAN TATAJI	18P35A0255

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of Mr. RAJESH MURARI


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Mr. RAJESH MURARI
Associate Professor

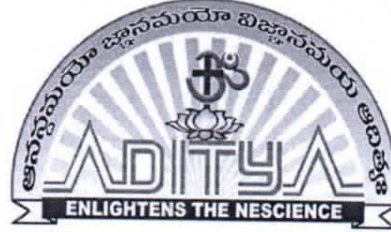

Head of the Department

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CERTIFICATE

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the thesis entitled “**CURRENT CONTROL OF THREE-PHASE GRID-CONNECTED PV INVERTERS USING ADAPTIVE PR CONTROLLER**” that is being submitted by

KATTA UDAYA BHASKAR 18P35A0216

KALA HARI KRISHNA 18P35A0211

GONDROTU NAVIN KUMAR 17P31A0212

THULLI SIVA PRASANNA KUMAR 18P35A0236

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of Mr. RAJESH MURARI


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Mr. RAJESH MURARI
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Associate Professor

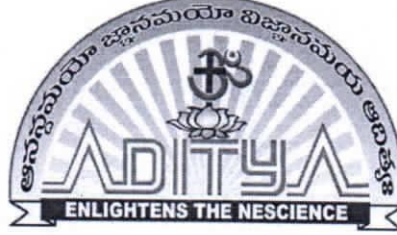

Head of the Department

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This is to certify that the thesis entitled "**Simulation of a grid connected Photo Voltaic**" is being submitted by

PEPAKAYALA MANIKANTA	18P35A0271
AMARADI SIVATEJASWAMY	18P35A0243
BATTULA VAMSI	18P35A0244
POTTURI V V S KRISHNA	18P35A0273
SAIMANIKANTA VARMA	

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of **Sri. R VENKATESH**

Project Guide


Mr. R VENKATESH
Assistant Professor

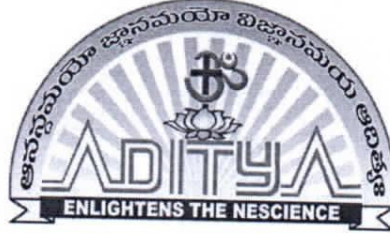

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This is to certify that the thesis entitled "**Grid Interfacing Schemes For Renewable Energy Sources**" that is being submitted by

PALAKATI JAYA KRISHNA	18P35A0230
ADDANKI SRAVANI	17P31A0204
BINGINA ADINARAYANA	18P35A0204
MATA VENKATA RAVI TEJA	18P35A0224

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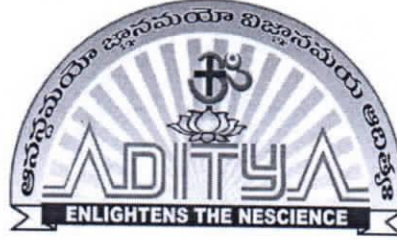

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Mr. RAJESH MURARI
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Aditya College of
Engineering & Technology
SURAMPALM- 533 437

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING




This is to certify that the thesis entitled “**Economic load dispatch using particle swarm optimization**” is being submitted by

GARAGA SIVA RAMAKRISHNA	17P31A0210
KONTHAM THARUN	18P35A0219
POLAMARASETTI DURGA PRASAD	18P35A0231
MUPPANA ESWARA SAI	18P35A0227
JAKKU GOWRI SHANKAR	17P31A0214

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of Mrs. KONETI VARALAKSHMI

Project Guide


Mrs. KONETI VARALAKSHMI
M.Tech,(Ph.D)
Sr. Assistant Professor

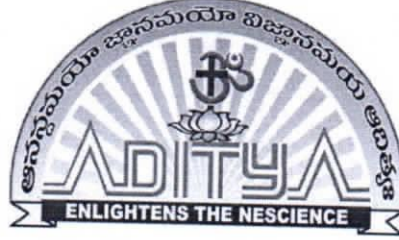

Head of the Department

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This is to certify that the thesis entitled “WIRELESS SENSOR NODE FOR GAS PIPELINE LEAK DETECTION” is being submitted by

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in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of **Mrs. KONETI VARALAKSHMI**

Project Guide


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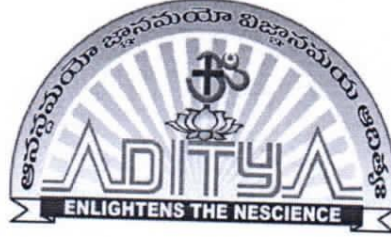

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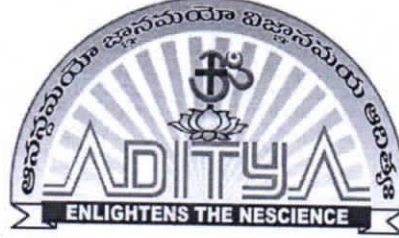

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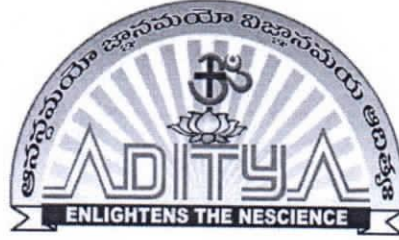

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