

CERTIFICATE



This is to certify that the Thesis entitled "A NEW GENERAL TOPOLOGY FOR CASCADED MULTILEVEL INVERTER WITH REDUCED NUMBER OF COMPONENTS BASED ON DEVELOPED H-BRIDGE" is being submitted by

P. ALEKHIYA
CH. SUMANTH
J.SURESH
G.PRAVEEN
K.TRIVENI


15P31A0230
16P35A0205
15P31A0217
15P31A0215
15P31A0218

In partial fulfillment of the requirements for the award of degree of **Bachelor of Technology** in **Electrical & Electronics Engineering**. It is a record of bonafide work carried out by him under my guidance and supervision of **Mr. K. DURGA PRASAD**, M.Tech


PROJECT GUIDE

Mr.K.DURGAPRASAD, M.Tech.

Assistant Professor


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Head of the Department
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This is to certify that the Thesis entitled " **SPLIT CONVERTER-FED SRM DRIVE FOR FLEXIBLE CHARGING IN EV/HEV APPLICATIONS** " is being submitted by

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This is to certify that the project report entitled

MODIFIED SINGLE-PHASE SINGLE-STAGE GRID-TIED FLYING INDUCTOR INVERTER WITH MPPT AND SUPPRESSED LEAKAGE CURRENT

Being submitted by

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A.RAJESH	16P35A0203
CH.SHIVA SANKAR	15P31A0206
M.SRINIVAS	16P35A0212
N.HARIBABU	15P31A0224

In the partial fulfillment of the requirement for the award of the degree of the Bachelor of Technology in Electrical and Electronics Engineering during the academic year 2015-2019. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

Signature of Project Guide

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In partial fulfillment of the requirement for the award of degree of the Bachelor of technology in **ELECTRICAL AND ELECTRONICS ENGINEERING**. It is the record of bonafide work, carried out by them under the esteemed guidance and supervision of **Mr.R.VENKATESH**, M.Tech., Assistant Professor.


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This is to certify that thesis is entitled "**Performance analysis of Cascaded H-bridge 13 level STATCOM during Switch Fault**" is being submitted by

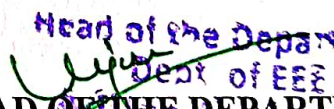
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Miss. P. Prasanna.	15P31A0233
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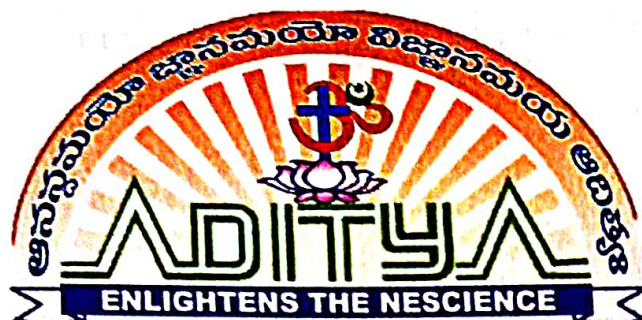
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This is to certify that the main project report entitled "LOAD FLOW STUDIES WITH UPFC POWER INJECTION MODEL" Being submitted by

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CH.S.S.G.KRISHNA	15P31A0207

In partial fulfillment of the requirement for the award of degree of the Bachelor of technology degree in ELECTRICAL AND ELECTRONICS ENGINEERING, during the academic year 2015-2019. The Results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma


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This is to certify that the project report entitled “OPTIMAL CONDUCTOR SELECTION AND CAPACITOR PLACEMENT FOR COST MINIMIZATION IN DISTRIBUTION SYSTEMS” being submitted by


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B.VENKATESH	15P31A0243
CH.S.L.N.VARMA	16P35A0221
R.VINDYANA	15P31A0242
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This is to certify that the main project report entitled "A CASCADED MULTILEVEL INVERTER TOPOLOGY BASED ON REDUCED COMPONENTS" Being submitted by


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CH.SASI KANTH	15P31A0244
A.KRISHNA VARMA	16P35A0218
P.BINDU DEEPIKA	15P31A0270
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In partial fulfillment of the requirement for the award of degree of the Bachelor of technology degree in ELECTRICAL AND ELECTRONICS ENGINEERING, during the academic year 2015-2019. The Results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma

Project guide

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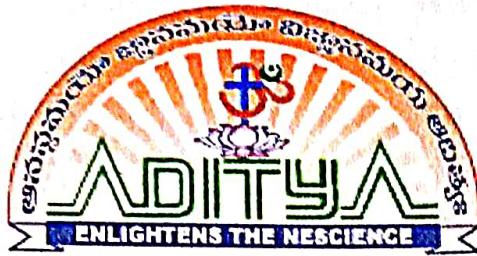
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Being submitted by

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N. SAI KIRAN	15P31A0225
P. SRIRAM CHAITANYA	15P31A0227
I.CHAKRADHAR	16P35A0209
P.V.S CHANDRASEKHAR REDDY	15P31A0228
G .RAMESH BABU	15P31A0211

in partial fulfillment of the requirement for the award of degree of the Bachelor of technology in **ELECTRICAL AND ELECTRONICS ENGINEERING**. It is the record of bonafide work, carried out by them under the esteemed guidance and supervision of **Mr. P.SRIDHAR**, M.Tech., Senior Assistant Professor.

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This is to certify that the main project report entitled “DISTRIBUTED GENERATION ALLOCATION WITH ON LOAD TAP CHANGER ON RADIAL DISTRIBUTION NETWORKS USING ADAPTIVE GENETIC ALGORITHM” Being submitted by

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SK.MAHABOOB BASHA	16P35A0232
P.HEMANTH	15P31A0269
D.MANIBABU	16P35A0222
T.NEELA MANIKANTA SRINIVAS	15P31A0276
K.VEERA VENKATA RAMANA	15P31A0256

In partial fulfillment of the requirement for the award of degree of the Bachelor of technology degree in ELECTRICAL AND ELECTRONICS ENGINEERING, during the academic year 2015-2019. The Results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma

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A

Project Report on

**“DTMF BASED REMOTE INDUSTRIAL LOAD AND/OR
AGRICULTURAL PUMP CONTROL”**

Submitted in partial fulfillment of the requirements for the award of the degree of

BACHELOR OF TECHNOLOGY

In

ELECTRICAL AND ELECTRONICS ENGINEERING

By

M.VEERABABU	16P35A0225
D.NARENDRA VARMA	16P35A0223
P.AHAMED SHAKHAN	15P31A0268
CH.V.A.R.SASTRY	15P31A0245
V.YASHWANTH KUMAR	15P31A0278
S.V.SAI KUMAR	15P31A0274

Under the esteemed guidance of

Mrs. K.VARALAKSHMI, M.Tech(Ph.D)

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This is to certify that the main project report entitled

“AN APPROACH TO VOLTAGE CONTROL IN DISTRIBUTION NETWORK WITH DG’S AND AVR’S ”

Being submitted by

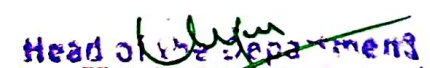
T.VANI	15P31A0277
K.T.V.V.BHAVANI PRASAD	15P31A0257
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SUPRIT KUMAR SAHA	15P31A0240

In partial fulfillment of the requirement for the award of degree of the Bachelor of technology degree in ELECTRICAL AND ELECTRONICS ENGINEERING ,during the academic year 2015-2019.The Results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma


Project guide

Mr. K.R.K.V. PRASAD, M.Tech (Ph.D),

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This is to certify that the project report entitled
“SMART DUSTBIN USING ARDUINO”

Being submitted by

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P.MOHAN SAI MADHUKARA	15P31A0267
M.SHARMILA	15P31A0262
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This is to certify that the project entitled "**Distributed Power Flow Controller For Power Quality Improvement In Distributed System**" is being submitted by

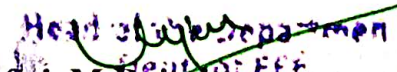
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This is to certify that the project report entitled "IMPACT OF NETWORK RECONFIGURATION ON LOSS ALLOCATION OF RADIAL DISTRIBUTION SYSTEM" Being submitted by

V.SYARUPA SWATHI
M.CHINASWAMY
M.P.N.S.SAI RAMESH
R.VAMSI KRISHNA

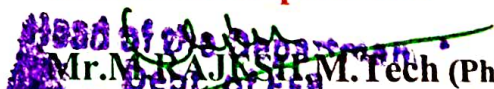
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S . V . M . Rajesh Babu	15P31A0234

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Mr. K.R.K.V. PRASAD, M.Tech (Ph.D),

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