

## CERTIFICATE



This is to certify that the project work entitled “**DOUBLY FED INDUCTION GENERATOR FOR WIND ENERGY CONVERSION SYSTEMS WITH INTEGRATED ACTIVE FILTER CAPABILITIES**”, is a being submitted by

<b>M OLIVIA RACHEL</b>	<b>(13P31A0229)</b>
<b>B SAI JAGADEESH</b>	<b>(13P31A0207)</b>
<b>P SARITHA RANI</b>	<b>(13P31A0241)</b>
<b>S VENKAT ANJANEYASWAMY</b>	<b>(14P35A0214)</b>

submitted in partial fulfillment of the requirements for the award of Bachelor of Technology degree in Electrical & Electronics Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

  
**PROJECT GUIDE**

**Mr. R. VENKATESH M.TECH, AMIE**

Asst. professor

  
**HEAD OF THE DEPARTMENT**

**Sri. J. Pavan, M.Tech(Ph.D)**

Head of the Department

**Department of Electrical & Electronics Engineering**  
**ADITYA COLLEGE OF ENGINEERING & TECHNOLOGY**

(APPROVED BY AICTE & AFFILIATED TO JNTUK, KAKINADA, ACCREDITED BY NAAC)

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ADITYANAGAR, ADB ROAD, SURAMPalem-533437

**2013-2017**

## CERTIFICATE



This is to certify that the report entitled **“POWER SYSTEM STSTE ESTIMATION USING WEIGHTED LEAST SQUARES TECHNIQUE”** being submitted by

**K.S.R.KRISHNA** 13P31A0222

**IQUBAL ANSARI** 13P31A0221

**CH.CHANDRA SEKHAR** 13P31A0210

**P.AKHIL** 13P31A0243

in partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL AND ELECTRONICS ENGINEERING** is record of benefited work, carried out by them under esteemed guidance and supervision of **Mr.Y.CHIRANJEEVI, M.Tech**

  
Project Guide

**Mr.Y.CHIRANJEEVI, M.Tech**

Assistant Professor

  
Signature of HOD

**Mr.J.PAVAN, M.Tech(Ph.D)**

Associate Professor

**Department of Electrical and Electronics Engineering**

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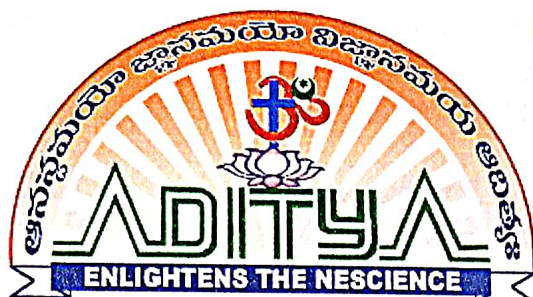
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2013-2017



## CERTIFICATE



This is to certify that the Distribution Power Flow Analysis project report entitled “Newton-Downhill Algorithm for Analysis” being submitted by

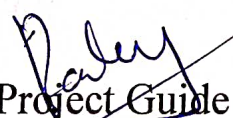
**P. Vamsi Krishna Reddy**      **13P31A0285**

**S. Manikanta**      **13P31A0292**

**Deepak Kumar**      **13P31A0261**

**A J Satya Manoj**      **11P31A0261**

in partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL & ELECTRONICS ENGINEERING** bonafied work, carried out by them under esteemed guidance and supervision of **Mr.K.R.K.V.PRASAD**.

  
Project Guide

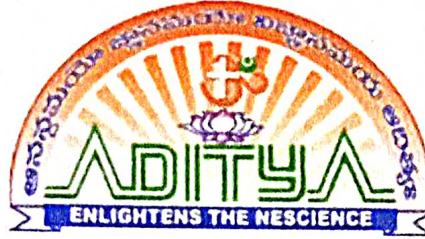
**Mr. K.R.K.V.PRASAD, M.Tech.,(Ph.D)**  
**Associate Professor**

  
Signature of HOD

**Sri. J.PAVAN, M.Tech.,(Ph.D)**  
**Associate Professor**

**Department of Electrical and Electronics Engineering**  
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( Recognised by UGC Section 2(f) of UGC Act 1956)  
**Aditya nagar, ADB ROAD, Surampalem E.G.DT-533437**  
**2013-2017**

# CERTIFICATE



This is to certify that the project work entitled “**INTELLIGENT ISLANDING AND SEAMLESS RECONNECTION TECHNIQUE FOR MICRO GRID WITH UPQC**”, is a being submitted by

<b>M.B S S KRISHNA</b>	<b>13P31A0233</b>
<b>V. JAYA KRISHNA</b>	<b>14P35A0215</b>
<b>T.RAVI</b>	<b>13P31A0248</b>
<b>R.NARESH</b>	<b>13P31A0244</b>

Partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

  
**PROJECT GUIDE**

**Mr. SK.ABDUL SATTAR**, M.Tech, AMIE  
Assistant Professor

  
**HEAD OF THE DEPARTMENT**

**Sri J.PAVAN**, M.Tech, (Ph.D)  
Associate Professor

**DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**  
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(RECOGNIZED BY UGC UNDER SECTION 2(F) OF UGC ACT 1956)  
ADITYANAGAR, ADB ROAD, SURAMPALEM-533437

**2013-2017**



## CERTIFICATE

This is to certify that the project work entitled "**GRID VOLTAGE SYNCHRONIZATION FOR DISTRIBUTED GENERATION SYSTEMS UNDER GRID FAULT CONDITIONS**", is a being submitted by

**M.N.SATYA PRASAD** (13P31A0273)

**SK.NAZEERUDDIN** (13P31A0290)

**M.AVINASH KUMAR** (13P31A0253)

**M.SIVA SANKAR** (13P31A0279)

submitted in partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

  
**PROJECT GUIDE**

**MR.SK.ABDUL SATTAR** M.Tech, AMIE

Assistant Professor,

  
**HEAD OF THE DEPARTMENT**

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Head of the Department,



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**2013-2017**

## CERTIFICATE



This is to certify the thesis entitled “ **OPERATION OF THREE - PHASE POWER CONVERTER CONNECTED TO A DISTRIBUTED SYSTEM**” is being submitted by

D. Joshua Victor (13P31A0262)  
G. Satish Kumar (13P31A0265)  
K. Lahari Sai Monica Priya (13P31A0270)  
P. Srinivasa Chowdary (13P31A0284)

Submitted in partial fulfillment of the requirements for the award of Bachelor of Technology degree in Electrical and Electronics Engineering, during the academic year 2013-2017

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**PROJECT GUIDE**

**Mrs. S.VANI , M.Tech**

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Dept. of Electrical and Electronics Engineering

  
**HEAD OF THE DEPARTMENT**

**Mr. J.PAVAN , M. Tech (Ph. D)**

Associate Professor

Dept. of Electrical and Electronics Engineering

**EXTERNAL EXAMINAR**

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

**ADITYA COLLEGE OF ENGINEERING AND TECHNOLOGY**

(Affiliated to JNTUK, Kakinada and Approved by AICTE, New Delhi)

**2013-2017**



# CERTIFICATE



This is to certify that the project work entitled **“ENERGY AUDITING IN ADITYA EDUCATIONAL INSTITUTIONS”**, is a being submitted by

<b>K. MOUNIKA</b>	<b>(13P31A0225)</b>
<b>G. PRASAD</b>	<b>(13P31A0218)</b>
<b>P. VEERA BABU</b>	<b>(14P51A0210)</b>
<b>R. NAGENDRA BABU</b>	<b>(14P35A0211)</b>

in partial fulfillment of the requirements for the award of Bachelor of Technology degree in Electrical & Electronics Engineering, during the academic year 2013-2017. It is record of bonafide work carried out by them under esteemed guidance and supervision of Mr. P. Sridhar, M.Tech

  
**PROJECT GUIDE**

**Mr. P. Sridhar, M.Tech**

Assistant Professor,

Electrical & Electronics Engineering

  
**HEAD OF THE DEPARTMENT**

**Sri J. Pavan, M.Tech(Ph.D)**

Associate Professor & HOD

Electrical & Electronics Engineering

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ADITYANAGAR, ADB ROAD, SURAMPALEM-533437

**2013-2017**

## CERTIFICATE



This is to certify that the main project report entitled "**DESIGN AND ANALYSIS OF ELECTRICAL SOLAR VEHICLE**" being submitted by

K. JANAKI DURGA PRASAD	14P35A0208
G. JAMES LEVIS	13P31A0216
B.VENKATA SATYASAI	13P31A0204
CH. SAI RAMA NAGAPRAVEEN	13P31A0211
B. SRINIVAS	13P31A0206

in partial fulfilment of requirement for the award of Bachelor of Technology degree in Electrical and Electronics Engineering, It is record of bonafied work, carried out by them under esteemed guidance and supervision of Sri J.PAVAN M.Tech, (Ph.D) .

  
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**Sri J.PAVAN** M.Tech, (Ph.D)

Associate Professor

Head of the Department

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**ADITYA COLLEGE OF ENGINEERING & TECHNOLOGY**

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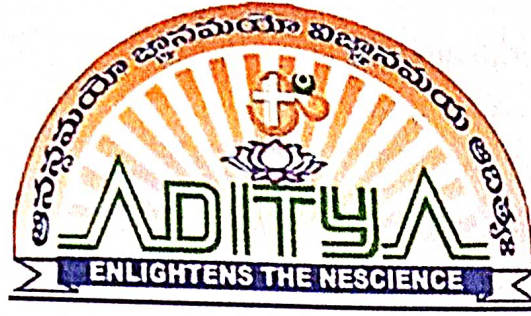
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2013-2017



## CERTIFICATE



This is to certify that the project work entitled “SPACE VECTOR BASED SYNCHRONIZED THREE-LEVEL DISCONTINUOUS PWM FOR MEDIUM-VOLTAGE HIGH-POWER VSI”, is a being submitted by

<b>B V S RAGHUNATH SASTRY</b>	<b>(13P31A0208)</b>
<b>N VEERA MANISAI</b>	<b>(13P31A0236)</b>
<b>M SWAMY</b>	<b>(13P31A0234)</b>
<b>S SATISH</b>	<b>(13P31A0245)</b>

Submitted in partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical & Electronics Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

  
**PROJECT GUIDE**

**Mr. V V DURGA PRASAD REDDY** M.Tech  
Assistant Professor

  
**HEAD OF THE DEPARTMENT**

**Mr. J. Pavan**, M.Tech(Ph.D)  
Head of the Department

**Department of Electrical & Electronics Engineering**  
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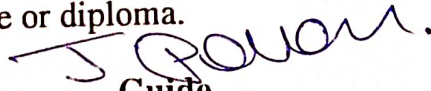


This is to certify that the project report entitled **“THREE PHASE FAULT INDICATION WITH AUTOMATIC RESET ON TEMPORARY FAULT AND PERMANENT TRIP WITH MICROCONTROLLER”** being submitted by

B.Sri Hari	14P35A0218
CH.Ramya Susmitha	13P31A0260
K.S.V.A.Naidu	13P31A0271
Rahul Kumar	13P31A0298
E.V.S.A.S.Sasikanth	13P31A0263

in partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL & ELECTRONICS ENGINEERING** is record of bonafied work, carried out by them under esteemed guidance and supervision of **Mr. J. PAVAN**

The results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma.

  
Guide

**Mr. J. PAVAN, M. Tech, (PhD)**

**Associate Professor & Head of department**

**Department of Electrical and Electronics Engineering**

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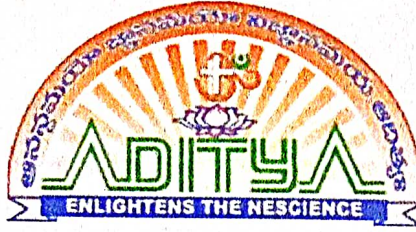
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Aditya Nagar, ADB Road, Surampalem-533437

2013-2017



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This is to certify that the project work entitled “**INTELLIGENT ISLANDING AND SEAMLESS RECONNECTION TECHNIQUE FOR MICRO GRID WITH UPQC**”, is a being submitted by

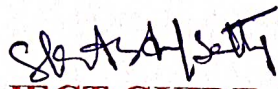
**M.B S S KRISHNA** 13P31A0233

**V. JAYA KRISHNA** 14P35A0215

**T.RAVI** 13P31A0248

**R.NARESH** 13P31A0244

Partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

  
**PROJECT GUIDE**

**Mr. SK.ABDUL SATTAR**, M.Tech, AMIE  
Assistant Professor

  
**HEAD OF THE DEPARTMENT**

**Sri J.PAVAN**, M.Tech, (Ph.D)  
Associate Professor

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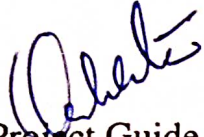
## CERTIFICATE

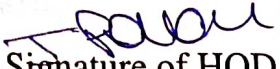


This is to certify that the report entitled “**THE DESIGN AND IMPLEMENTATION OF SOLAR TRACKING GENERATING POWER SYSTEM**” being submitted by

<b>S.V.V.VARA PRASAD</b>	<b>14P35A0228</b>
<b>I.SURYA TEJA</b>	<b>14P35A0222</b>
<b>N.MANIKANTA</b>	<b>13P31A0280</b>
<b>O.KUSARAJU</b>	<b>13P31A0282</b>

in partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL AND ELECTRONICS ENGINEERING** is record of benefited work, carried out by them under esteemed guidance and supervision of **Mr. R.VENKATESH, M.Tech**

  
Project Guide

  
Signature of HOD

**Mr. R.VENKATESH, M.Tech**

**Mr.J.PAVAN, M.Tech(Ph.D)**

Assistant Professor

Associate Professor

**Department of Electrical and Electronics Engineering**

**ADITYA COLLEGE OF ENGINEERING AND TECHNOLOGY**

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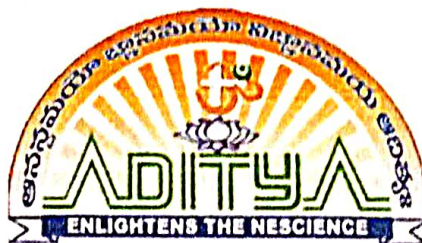
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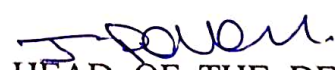
This is to certify that the project report entitled “**DESIGN AND IMPLEMENTATION OF 11-LEVEL INVERTER WITH FACTS CAPABILITY FOR DISTRIBUTED ENERGY SYSTEMS**” is being submitted by

MANIDEEP RAJ	13P31A0276
G D D TEJASRI	14P35A0221
G KIRAN KUMAR	14P35A0220
P K S KUMAR	13P31A0283

In partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL & ELECTRONICS ENGINEERING** is record of Bonafied work, carried out by them under esteemed guidance and supervision of **Mr. S. REDDY RAMESH**

The results embodied in this project report have not been submitted to any other institute Or university for the award of any degree or diploma.

  
PROJECT GUIDE  
**Mr. S REDDY RAMESH** M. TECH, AMIE.

  
HEAD OF THE DEPARTMENT  
**Mr. J PAVAN** M. TECH, (PIL D),

Department of Electrical & Electronics Engineering

**ADITYA COLLEGE OF ENGINEERING & TECHNOLOGY**

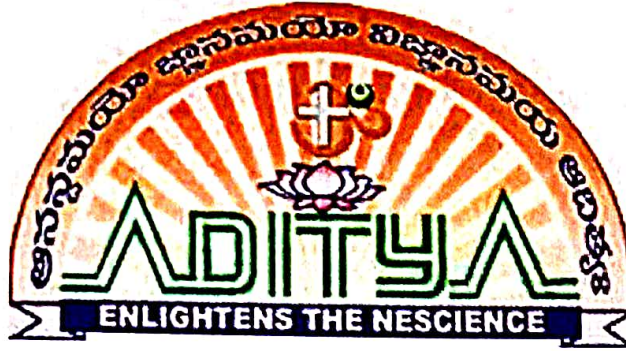
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Aditya Nagar, ADB Road, Surampalem-533437

2013-2017

# CERTIFICATE



This is to certify that the project work entitled “**A GRID INTERFACING SCHEME FOR RENEWABLE ENERGY RESOURCES**”, is being submitted by

**A.SAIPHALGUNA**

**13P31A0203**

**D.SRIHARIREDDY**

**13P31A0213**

**P.SUBRAHMANYAM**

**13P31A0240**

**SHAIK SARDAR**

**14P35A0213**

Submitted in partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

  
**PROJECT GUIDE**

**Mr. S. REDDY RAMESH**.M.TECH.AMIE

  
**HEAD OF THE DEPARTMENT**

**Mr. J. PAVAN**.M.TECH.(Ph.D)

**Department of Electrical & Electronics Engineering**

**ADITYA COLLEGE OF ENGINEERING & TECHNOLOGY**

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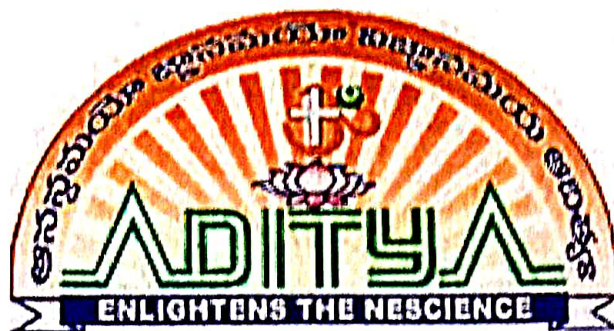
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**2013-2017**



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This is to certify that the project work entitled “**A GRID INTERFACING SCHEME FOR RENEWABLE ENERGY RESOURCES**”, is being submitted by

<b>A.SAIPHALGUNA</b>	<b>13P31A0203</b>
<b>D.SRIHARI REDDY</b>	<b>13P31A0213</b>
<b>P.SUBRAHMANYAM</b>	<b>13P31A0240</b>
<b>SHAIK SARDAR</b>	<b>14P35A0213</b>

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**Mr. S. REDDY RAMESH**, M.TECH., AMIE

  
**HEAD OF THE DEPARTMENT**

**Mr. J. PAVAN**, M.TECH., (Ph.D)

## Department of Electrical & Electronics Engineering

### ADITYA COLLEGE OF ENGINEERING & TECHNOLOGY

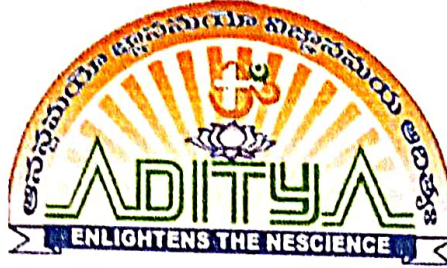
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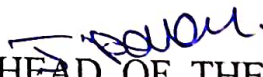
This is to certify that the project report entitled "**DESIGN AND IMPLEMENTATION OF 11-LEVEL INVERTER WITH FACTS CAPABILITY FOR DISTRIBUTED ENERGY SYSTEMS**" is being submitted by

MANIDEEP RAJ	13P31A0276
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G KIRAN KUMAR	14P35A0220
P K S KUMAR	13P31A0283

In partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL & ELECTRONICS ENGINEERING** is record of Bonafied work, carried out by them under esteemed guidance and supervision of **Mr. S. REDDY RAMESH**

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PROJECT GUIDE  
**Mr. S REDDY RAMESH** M. TECH, AMIE.

  
HEAD OF THE DEPARTMENT  
**Mr. J PAVAN** M. TECH, (PH. D).

Department of Electrical & Electronics Engineering

**ADITYA COLLEGE OF ENGINEERING & TECHNOLOGY**

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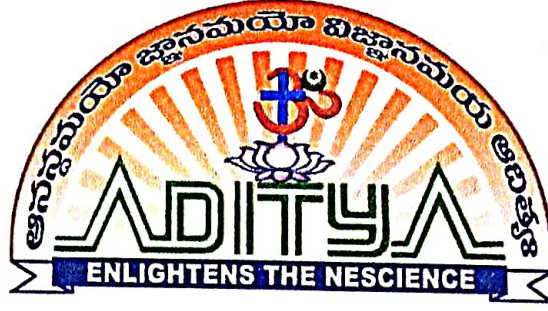
(Recognized by UGC under section 2(f) of UGC Act 1956)

Aditya Nagar, ADB Road, Surampalem-533437

2013-2017



## CERTIFICATE



This is to certify that the main project report entitled “**Control of Reduced Rating Dynamic Voltage Restorer with a Battery Energy Storage System**” is being submitted by

G. MADHU PHANINDRA SAI	(13P31A0217)
Md.AHAMADI	(14P35A0202)
A.SUDHEER	(14P35A0201)
S.RAHUL SINGH	(13P31A0246)

in the partial fulfillment of the requirements for the award of degree of **Bachelor of Technology** in **Electrical and Electronics Engineering** from **Jawaharlal Nehru Technological University Kakinada**. It is a record of bonafide work, carried out by them under esteemed guidance and supervision of **Mr. M.P.SUBBARAJU** M. Tech.,

  
PROJECT GUIDE

**Mr. M.P.SUBBARAJU** M. Tech.,  
Asst professor

  
HEAD OF THE DEPARTMENT

**Sri. J.PAVAN**, M. Tech.,(PhD)  
Associate Professor

Department of Electrical and Electronics Engineering  
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(Affiliated to JNTUK, Kakinada & Approved by AICTE, New Delhi)

Surampalem, A.D.B.ROAD, East Godavari District

2013-2017

## CERTIFICATE




This is to certify that the report entitled **“AN ENHANCED GRID CURRENT COMPENSATOR FOR GRID - CONNECTED DISTRIBUTED GENERATION UNDER NONLINEAR LOADS AND GRID VOLTAGE DISTORTIONS”**

being submitted by

S. NAVEEN KUMAR	13P31A0288
B. MANOHAR REDDY	13P31A0254
G. ANIL	13P31A0266
N. BALAJI PAWAN TANUJ	13P31A0281

in partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL AND ELECTRONICS ENGINEERING** is record of benefited work, carried out by them under esteemed guidance and supervision of **Mr. M.P.SUBBARAJU, M.Tech**

  
Project Guide

  
Signature of HOD

**Mr.M.P.SUBBARAJU, M.Tech**

**Mr.J.PAVAN, M.Tech(Ph.D)**

Assistant Professor

Associate Professor

Department of Electrical and Electronics Engineering

**ADITYA COLLEGE OF ENGINEERING AND TECHNOLOGY**

(Approved by AICTE, Affiliated to JNTUK, Kakinada & Accredited by NAAC)

(Recognized by UGC under section 2(f) of UGC Act 1956)

Aditya Nagar, ADB Road, Surampalem-533437

2013-2017



## CERTIFICATE



This is to certify that the report entitled "**TOUCH SCREEN & ZIGBEE BASED HOME AND INDUSTRIAL AUTOMATION SYSTEM**" being submitted by

<b>B.VENKATESWARA RAO</b>	<b>14P35A0219</b>
<b>K.GOPALA KRISHNA</b>	<b>14P35A0223</b>
<b>A.ANUBABU</b>	<b>14P35A0216</b>
<b>MD.AMEER SHARIEF</b>	<b>13P31A0297</b>

in partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL AND ELECTRONICS ENGINEERING** is record of benefited work, carried out by them under esteemed guidance and supervision of **Miss.V.U.P.LAVANYA, M.Tech**

  
Project Guide

  
Signature of HOD

**Miss.V.U.P.LAVANYA**, M.Tech, AMIE

**Sri.J.PAVAN**, M.Tech(Ph.D), MIE

Assistant Professor

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2013-2017

# CERTIFICATE



This is to certify that the project entitled **"A Three-Phase Interleaved Converter with Active Clamp for Fuel Cells"** is being submitted by

NUSRAT ANJUM	13P31A0238
M. DURGA SHYAMALA	13P31A0232
K.SATYANARAYANA	13P31A0226
M. VINAY	13P31A0231

in partial fulfillment for the award of degree of **B.Tech in Electrical and Electronics Engineering** from **Jawaharlal Nehru Technological University Kakinada** is a record of bonafide work carried out by them at **Aditya college of engineering & Technology**. The results embodied in this project have not been submitted to any other University or Institute for the award of any degree or diploma.

  
**PROJECT GUIDE**

**Miss. V.U.P Lavanya, M.Tech, AMIE**  
**Assistant Professor**

  
**HEAD OF THE DEPARTMENT**

**Sri. J. Pavan, M.Tech (Ph.D), MIE**  
**Associate Professor**

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**  
**ADITYA COLLEGE OF ENGINEERING & TECHNOLOGY**

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Surampalem, East Godavari District, A.P, India

**2013-2017**



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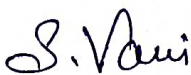



This is to certify that the project work entitled **"FAULT CURRENT INTERRUPTION BY THE DYNAMIC VOLTAGE RESTORER"** is being submitted by

K. ABBAI REDDY	13P31A0223
J. D L NARAYANA	14P35A0206
D. S L SUDHEER	13P31A0212
M. VENKATESH	13P31A0230

in partial fulfillment of the requirements for the award of degree of **B.Tech in Electrical and Electronics Engineering** to **Jawaharlal Nehru Technological University Kakinada** is a record of bonafide work carried out by him under my guidance and supervision.

The results embodied in this Thesis have not been submitted to any other University or Institute for the award of any degree or diploma.

  
**PROJECT GUIDE**  
Mrs. S. Vani, M.Tech,  
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**HEAD OF THE DEPARTMENT**  
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## CERTIFICATE



This is to certify that the report entitled **“POWER SYSTEM STATE ESTIMATION USING WEIGHTED LEAST SQUARES TECHNIQUE”** being submitted by

**K.NAVEEN** 14P35A0224

**M.TEJA** 14P35A0227

**K.KONDALA RAO** 13P31A0268

**K.SOMESH** 13P31A0269

in partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL AND ELECTRONICS ENGINEERING** is record of benefited work, carried out by them under esteemed guidance and supervision of **Mr.Y.CHIRANJEEVI, M.Tech**

  
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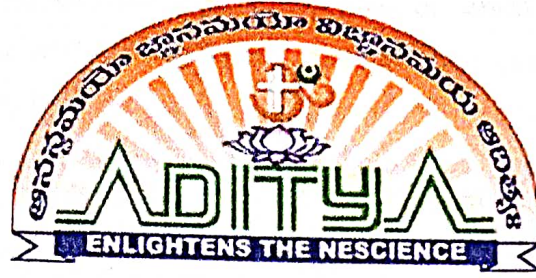
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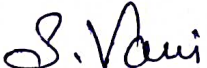



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J. D L NARAYANA	14P35A0206
D. S L SUDHEER	13P31A0212
M. VENKATESH	13P31A0230

in partial fulfillment of the requirements for the award of degree of **B.Tech in Electrical and Electronics Engineering** to **Jawaharlal Nehru Technological University Kakinada** is a record of bonafide work carried out by him under my guidance and supervision.

The results embodied in this Thesis have not been submitted to any other University or Institute for the award of any degree or diploma.

  
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## CERTIFICATE

This is to certify that the project work entitled "GRID VOLTAGE SYNCHRONIZATION FOR DISTRIBUTED GENERATION SYSTEMS UNDER GRID FAULT CONDITIONS", is a being submitted by

**M.N.SATYA PRASAD** (13P31A0273)

**SK.NAZEERUDDIN** (13P31A0290)

**M.AVINASH KUMAR** (13P31A0253)

**M.SIVA SANKAR** (13P31A0279)

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**PROJECT GUIDE**

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**2013-2017**



# CERTIFICATE



This is to certify that the project work entitled "Improvement of Power Quality by Using Power Conditioner Integrated with Ultra Capacitor", is a being submitted by

**V VV S VARA PRASAD (13P31A0294)**

**R SUDHEER KUMAR (13P31A0287)**

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*K. Vijaya Lakshmi*  
**PROJECT GUIDE**

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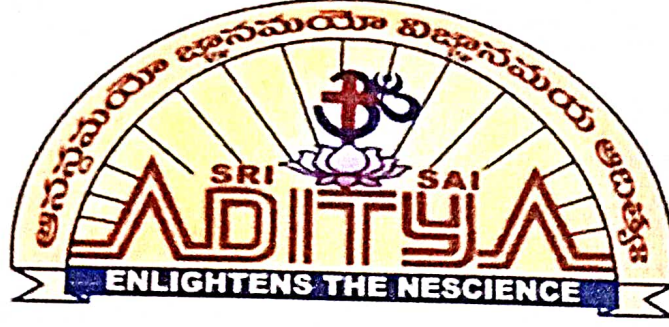
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ADITYANAGAR, ADB ROAD, SURAMPALEM-533437

**2013-2017**

## CERTIFICATE



This is to certify the thesis entitled **“Stability and Voltage Balance Control of A Modular Converter with Multi winding High-Frequency Transformer”** is being submitted by

C.SAI TEJA (13P31A0258)

B.M.S.KUMAR (13P31A0257)

B.V.S.SEKHAR (13P31A0255)

M.CHALAPATHI (13P31A0277)

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**Sr. Assistant Professor**

**HEAD OF THE DEPARTMENT:**

**Sri J. Pavan. M. Tech,(Ph. D)**

**Associate Professor**

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

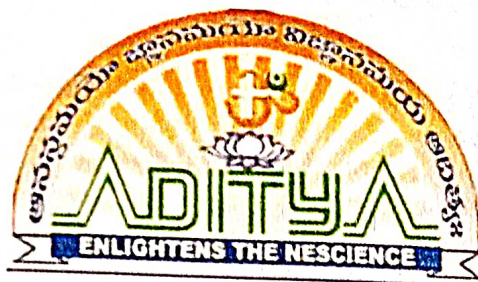
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**(RECOGNIZED BY UGC UNDER SECTION 2(F) OF UGC ACT 1956)**

**ADITYANAGAR, ADB ROAD, SURAMPALEM-533437**



## CERTIFICATE



This is to certify that the project work entitled “SPACE VECTOR BASED SYNCHRONIZED THREE-LEVEL DISCONTINUOUS PWM FOR MEDIUM-VOLTAGE HIGH-POWER VSI”, is a being submitted by

**K GNANA PRABHA** (13P31A0272)

**A N S VINAY** (13P31A0252)

**S SNEHA PRIYA** (13P31A0291)

**G S S KRISHNA YADAV** (13P31A0264)

**K S S S TEJA** (13KA1A0203)

Submitted in partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical & Electronics Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

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Assistant Professor

**HEAD OF THE DEPARTMENT**

**Sri. J. PAVAN, M.TECH (Ph.D)**

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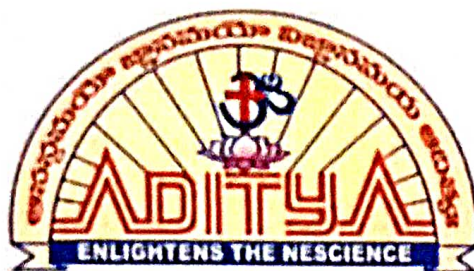
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ADITYANAGAR, ADB ROAD, SURAMPALEM-533437

**2013-2017**



## CERTIFICATE



This is to certify that the main project report entitled **"SOLAR POWERED IRRIGATION SYSTEM"** Being submitted by

V.KASI	14P35A0229
K.VINEELA	14P35A0226
P.YOSEPU	14P35A0231
S.B.BALAJI	14P35A0230
P.SAI SANDEEP	13KA1A0204

In partial fulfillment of the requirement for the award of the Degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL AND ELECTRONICS ENGINEERING** is record of bonafide work, carried out by them under the esteemed guidance and supervision of Mr.K.DURGA PRASAD M.Tech.

  
Project Guide

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Assistant Professor

  
Head of the Department

**Sri J.PAVAN M.Tech.(Ph.D.)**

Associate Professor & HOD

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(2013-2017)