

## CERTIFICATE



This is to certify that the Thesis entitled "A NEW GENERAL TOPOLOGY FOR CASCADED MULTILEVEL INVERTER WITH REDUCED NUMBER OF COMPONENTS BASED ON DEVELOPED H-BRIDGE" is being submitted by

P. ALEKHYA  
CH. SUMANTH  
J.SURESH  
G.PRAVEEN  
K.TRIVENI

15P31A0230  
16P35A0205  
15P31A0217  
15P31A0215  
15P31A0218

In partial fulfillment of the requirements for the award of degree of **Bachelor of Technology in Electrical & Electronics Engineering**. It is a record of bonafide work carried out by him under my guidance and supervision of **Mr. K. DURGA PRASAD, M.Tech**

  
PROJECT GUIDE

**Mr.K.DURGAPRASAD, M.Tech.**

Assistant Professor

  
HEAD OF THE DEPARTMENT

Head of the Department  
**Mr. M. RAJESH, M.Tech, M.I.S.T.E.(PhD)**

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**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

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This is to certify that the Thesis entitled " SPLIT CONVERTER-FED SRM DRIVE FOR FLEXIBLE CHARGING IN EV/HEV APPLICATIONS " is being submitted by

Miss. Ch. Priyanka  
Mr. D. Joshi Richard  
Mr. A.S.G. Naveen  
Mr. G. Pavan Kumar  
Mr. A. Venkata Madhu

16P35A0206  
15P31A0208  
15P31A0202  
16P35A0207  
15P31A0235

in partial fulfillment of the requirements for the award of degree of Bachelor of Technology in Electrical & Electronics Engineering. It is a record of bonafide work carried out by him under my guidance and supervision of Mr. R.Venkatesh M.Tech

  
PROJECT GUIDE

Mr. R.Venkatesh, M.Tech.

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## CERTIFICATE



This is to certify that the project report entitled

### **MODIFIED SINGLE-PHASE SINGLE-STAGE GRID-TIED FLYING INDUCTOR INVERTER WITH MPPT AND SUPPRESSED LEAKAGE CURRENT**

Being submitted by

N.V.V.SATYANARAYANA	15P31A0223
A.RAJESH	16P35A0203
CH.SHIVA SANKAR	15P31A0206
M.SRINIVAS	16P35A0212
N.HARIBABU	15P31A0224

In the partial fulfillment of the requirement for the award of the degree of the Bachelor of Technology in Electrical and Electronics Engineering during the academic year 2015-2019. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

Signature of Project Guide

Mr. V V DURGA PRASAD REDDY, M.Tech  
Assistant Professor

Head of the Department  
Dept of EEE  
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Signature of H.O.D

Mr. RAJESH.MURARI, M.Tech, (Ph.D)  
Associate Professor

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**2015-2019**

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*This is to certify that the project report entitled "DESIGN OF HIGH POWER DENSITY  
INVERTER FOR AIRCRAFT APPLICATION" Being submitted by*

M.A MAHALAKSHMI	16P35A0227
G.B.R.KALYAN	15P31A0248
N.TANUJA	15P31A0266
MUNNA KUMAR	15P31A0264
P.SAI KRISHNA	15P31A0272
K.RAJESH	15P31A0258

In partial fulfillment of the requirement for the award of degree of the Bachelor of technology in **ELECTRICAL AND ELECTRONICS ENGINEERING**. It is the record of bonafide work, carried out by them under the esteemed guidance and supervision of **Mr.R.VENKATESH, M.Tech.**, Assistant Professor.

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This is to certify that thesis is entitled "**Performance analysis of Cascaded H-bridge 13 level STATCOM during Switch Fault**" is being submitted by

Miss. K.Jyothisuma.	15P31A0219
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Miss. T. Priyanka.	15P31A0238
Mr. P. Prem kumar.	15P31A0231
Miss. P. Prasanna.	15P31A0233
Mr. O. Manikanta swamy.	15P31A0226

in partial fulfillment of the requirements for the award of degree of Bachelor of Technology in Electrical & Electronics Engineering. It is a record of bonafide work carried out by him under my guidance and supervision of Mrs.K.Varalakshmi M.Tech, M.I.S.T.E.,(PhD)

  
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# CERTIFICATE

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



This is to certify that the thesis entitled "**DISTRIBUTED POWER FLOW CONTROLLER FOR POWER QUALITY IMPROVEMENT IN DISTRIBUTED SYSTEM**" that is being submitted by


Mr. AMALAPURAM SIVA SAI SATYA DURGA PRASAD	16P35A0202
Mr. GUNDIBILLI SIVA PRASAD KUMAR	15P31A0216
Mr. MANDAPAKA MADHAVA NAIDU	15P31A0224
Mr. PENDEM DURGA PRAKASH	16P35A0215
Miss KADIYAM RAVI CHANDRIKA	16P35A0210


in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of Miss.V.U.P.LAVANYA

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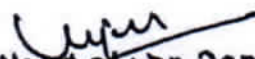
This is to certify that the main project report entitled "LOAD FLOW STUDIES WITH UPFC POWER INJECTION MODEL" Being submitted by

D.NARESH	15P31A0209
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M.E.V.VARDHANA GOWDA	15P31A0222
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In partial fulfillment of the requirement for the award of degree of the Bachelor of technology degree in ELECTRICAL AND ELECTRONICS ENGINEERING, during the academic year 2015-2019. The Results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma

  
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### DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING



*This is to certify that the project report entitled "OPTIMAL CONDUCTOR SELECTION AND CAPACITOR PLACEMENT FOR COST MINIMIZATION IN DISTRIBUTION SYSTEMS" being submitted by*

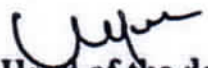
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B.VENKATESH	15P31A0243
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N.V.L.NARAYANA	15P31A0265

in partial fulfillment of the requirement for the award of degree of the Bachelor of technology in **ELECTRICAL AND ELECTRONICS ENGINEERING**. It is the record of bonafide work, carried out by them under the esteemed guidance and supervision of **Mr. K.SANTHOSH KUMAR**, M.Tech, AMIE, Assistant Professor.

  
Project guide

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Assistant Professor

  
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Associate Professor

  
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This is to certify that the main project report entitled "A CASCADED MULTILEVEL INVERTER TOPOLOGY BASED ON REDUCED COMPONENTS" Being submitted by


T.LAKSHMAN KUMAR	16P35A0234
CH.SASI KANTH	15P31A0244
A.KRISHNA VARMA	16P35A0218
P.BINDU DEEPIKA	15P31A0270
S.SAI REDDY	15P31A0273

In partial fulfillment of the requirement for the award of degree of the Bachelor of technology degree in ELECTRICAL AND ELECTRONICS ENGINEERING, during the academic year 2015-2019. The Results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma

Project guide

Head of the department

  
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This is to certify that the thesis entitled "**COMPREHENSIVE ANALYSIS AND REDUCTION OF TORQUE RIPPLES IN THREE PHASE FOUR SWITCH INVERTER FED PMSM DRIVES USING SPACE VECTOR PULSE WIDTH MODULATION**" that is being submitted by

Miss DESALINKA SRI SATYA DURGA  
Miss GANJA KALYANI LAKSHMI DURGA  
Miss GANUGULA NAGA MOUNIKA  
Mr. VASAMSETTI MALLI BABU  
Mr. VURA VEERA VENKATA MANI KUMAR

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15P31A0214  
15P31A0239  
16P35A0217

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of Mr.R.SRINIVAS

**Project Guide**

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**Head of the Department**

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# CERTIFICATE



*This is to certify that the project report entitled*

## **“OPTIMAL CONDUCTOR SELECTION AND CAPACITOR PLACEMENT FOR COST MINIMIZATION IN DISTRIBUTION SYSTEMS”**

*Being submitted by*

V.VENKATESH	16P35A0216
N. SAI KIRAN	15P31A0225
P. SRIRAM CHAITANYA	15P31A0227
I.CHAKRADHAR	16P35A0209
P.V.S CHANDRASEKHAR REDDY	15P31A0228
G .RAMESH BABU	15P31A0211

in partial fulfillment of the requirement for the award of degree of the Bachelor of technology in **ELECTRICAL AND ELECTRONICS ENGINEERING**. It is the record of bonafide work, carried out by them under the esteemed guidance and supervision of **Mr. P.SRIDHAR, M.Tech., Senior Assistant Professor.**

Project guide

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Senior Assistant Professor

Head of the Department  
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This is to certify that the main project report entitled "DISTRIBUTED GENERATION ALLOCATION WITH ON LOAD TAP CHANGER ON RADIAL DISTRIBUTION NETWORKS USING ADAPTIVE GENETIC ALGORITHM" Being submitted by

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SK.MAHABOOB BASHA	16P35A0232
P.HEMANTH	15P31A0269
D.MANIBABU	16P35A0222
T.NEELA MANIKANTA SRINIVAS	15P31A0276
K.VEERA VENKATA RAMANA	15P31A0256

In partial fulfillment of the requirement for the award of degree of the Bachelor of technology degree in ELECTRICAL AND ELECTRONICS ENGINEERING, during the academic year 2015-2019. The Results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma

Head of the Department  
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This is to certify that the main project report entitled

**"AN APPROACH TO VOLTAGE CONTROL IN DISTRIBUTION NETWORK WITH DG'S AND AVR'S "**

*Being submitted by*

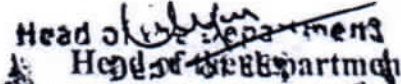
T.VANI	15P31A0277
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*This is to certify that the project report entitled*  
**“SMART DUSTBIN USING ARDUINO”**

*Being submitted by*

<b>M.BALA SHANKAR</b>	<b>16P35A0226</b>
<b>N.SURESH KUMAR</b>	<b>16P35A0228</b>
<b>P.MOHAN SAI MADHUKARA</b>	<b>15P31A0267</b>
<b>M.SHARMILA</b>	<b>15P31A0262</b>
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<b>M.PRAVEEN KUMAR</b>	<b>15P31A0260</b>

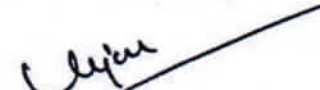
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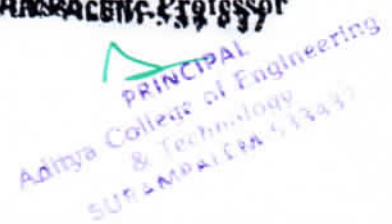


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**Senior Assistant Professor**

  
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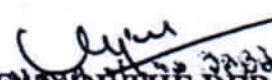
R. SATTI BABU	16P35A0229
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K. BHARGAVA RAVITEJA	15P31A0254
M. SUNEELA	15P31A0263
V.DURGAPRASAD	15P31A0279

in partial fulfillment of the requirements for the award of degree of **Bachelor of Technology in Electrical & Electronics Engineering**. It is a record of bonafide work carried out by him under my guidance and supervision of Mr. K. DURGA PRASAD<sub>M.Tech</sub>

  
PROJECT GUIDE

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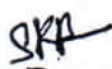


This is to certify that the project entitled "Distributed Power Flow Controller For Power Quality Improvement In Distributed System" is being submitted by

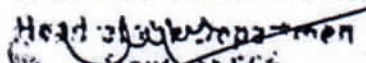
D.ROOPADEV	15P31A0246
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K.SANTHOSH REDDY	15P31A0252
K.ANIL KUMAR	15P31A0253
T.D.S.PRASAD	16P35A0233

In partial fulfillment for the award of degree of B.Tech in Electrical and Electronics Engineering from Jawaharlal Nehru Technological University Kakinada is a record of bonafide work carried out by them at Aditya college of engineering & Technology. The results embodied in this project have not been submitted to any other University or Institute for the award of any degree or diploma.

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Assistant Professor


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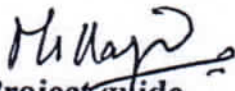


This is to certify that the project report entitled "IMPACT OF NETWORK RECONFIGURATION ON LOSS ALLOCATION OF RADIAL DISTRIBUTION SYSTEM" Being submitted by

V.SYARUPA SWATHI  
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M.P.N.S.SAI RAMESH  
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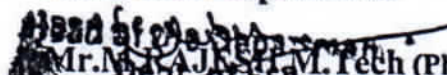
In partial fulfillment of the requirement for the award of degree of the Bachelor of technology in ELECTRICAL AND ELECTRONICS ENGINEERING. It is the record of bonafide work, carried out by them under the esteemed guidance and supervision of **Mr. M.P.SUBBA RAJU, M.Tech., Assistant Professor.**

  
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in partial fulfillment of the requirements for the award of degree of Bachelor of Technology in Electrical & Electronics Engineering. It is a record of bonafide work carried out by him under my guidance and supervision of Mr. M.Rajesh, M.Tech., (Ph.D.)

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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This is to certify that the main project report entitled

**"AN APPROACH TO VOLTAGE CONTROL IN DISTRIBUTION NETWORK WITH DG'S AND AVR'S "**

*Being submitted by*

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In partial fulfilment of the requirement for the award of degree of the Bachelor of technology degree in ELECTRICAL AND ELECTRONICS ENGINEERING ,during the academic year 2015-2019.The Results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma

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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



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