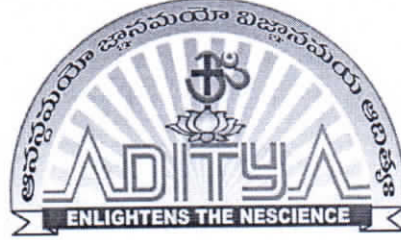


CERTIFICATE

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING




This is to certify that the thesis entitled “Design and analysis of electrical solar vehicle” that is being submitted by


YELLUBANDI SUDHEER SYAM LOKESH	13P35A0227
POSUKURI ANIL KUMAR	14P35A0212
PRADEEPTHI BAKKI	13P31A0224
PATCHAMALLA RAJA	13P35A0250

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of **Sri.K.DURGAPRASAD**

Project Guide


Sri.K.DURGA PRASAD
Associate Professor

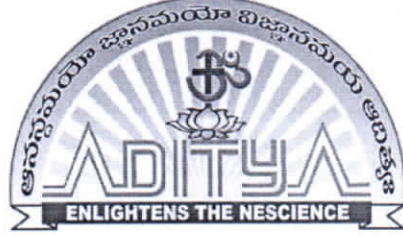
Head of the Department


Mr. J PAVAN
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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING




This is to certify that the thesis entitled “Newton-downhill algorithm for distribution power flow analysis that is being submitted by

MR.GUMMANAMPATI MASTAN VALI	13P31A0230
MR.KILARI KISHORE	14P35A0209
MR.CHANDINA NAVEEN	13P31A0209
MR.VELDUTI VEERA	13P31A0249

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of **Sri.K.R.K.V.PRASAD**

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING




This is to certify that the thesis entitled “Integrated inverter/converter circuit and control technique of motor drives with dual mode control for EV/HEV application” that is being submitted by


MISS DIVYA DEEPTI	13P31A0214
MR. KARUPOTHU RAMESH	14P35A0207
MR. SIVA RAMA KRISHNA	13P31A0247
MR. GONTENA SAIRAM	13P31A0219

in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of **MR.R.SRINIVAS**

Project Guide


MR. R. SRINIVAS
Associate Professor

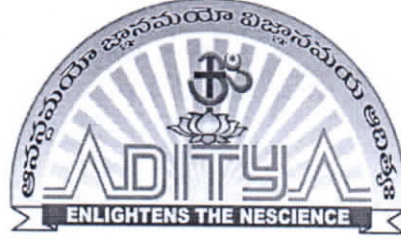
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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING




This is to certify that the thesis entitled “**CONTROL OF GRID SIDE INVERTER BY USING HYSTERESIS CURRENT CONTROL TECHNIQUE**” that is being submitted by

MR.ALLUMALLU KALYAN	13P31A0293
MR.VANGA SIVA RAMA KISHORE	14P35A0217
MR.PRADIP BIKRAM SHAH	13P31A0296
MR.BIKRAM KUMAR MISHRA	13P31A0256


in fulfilment of the requirements for the award of degree of **B.Tech in ELECTRICAL AND ELECTRONICS ENGINEERING** is a record of bonified work carried out by them under the guidance and supervision of **MR.R.SRINIVAS**

Project Guide


MR.R.SRINIVAS
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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING




This is to certify that the thesis entitled "Design and analysis of solar electric vehicle" that is being submitted by


MR.ADIGARLA UDAY	13P31A0251
MISS.VEDIDA SAI KALYANI	13P31A0295
MR.PYLA BABJI	13P31A0286
MR.MALIREDDY JYOTHI KUMAR	13P31A0274

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Project Guide


K.SANTHOSH KUMAR
Associate Professor

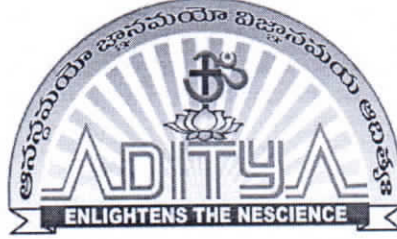
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CERTIFICATE

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This is to certify that the thesis entitled "Design and analysis of solar electric vehicle" that is being submitted by

MR. CHANDU GOPU KRISHNA

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MR. DUMMETI DHARMA TEJA

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MR. GUNTURI SAI RAKESH VARMA


14P35A0205

MR. ADAM CHANUKYA VARMA

13P31A0201

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CERTIFICATE



This is to certify that the project work entitled "DOUBLY FED INDUCTION GENERATOR FOR WIND ENERGY CONVERSION SYSTEMS WITH INTEGRATED ACTIVE FILTER CAPABILITIES", is a being submitted by

M OLIVIA RACHEL	(13P31A0229)
B SAI JAGADEESH	(13P31A0207)
P SARITHA RANI	(13P31A0241)
S VENKAT ANJANEYASWAMY	(14P35A0214)

submitted in partial fulfillment of the requirements for the award of Bachelor of Technology degree in Electrical & Electronics Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.


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Mr. R. VENKATESH M.TECH, AMIE
Asst. professor


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2013-2017


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CERTIFICATE



This is to certify that the report entitled **"POWER SYSTEM STATE ESTIMATION USING WEIGHTED LEAST SQUARES TECHNIQUE"** being submitted by

K.S.R.KRISHNA	13P31A0222
IQUBAL ANSARI	13P31A0221
CH.CHANDRA SEKHAR	13P31A0210
P.AKHIL	13P31A0243

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Mr.Y.CHIRANJEEVI, M.Tech

Assistant Professor


Signature of HOD

Mr.J.PAVAN, M.Tech(Ph.D)

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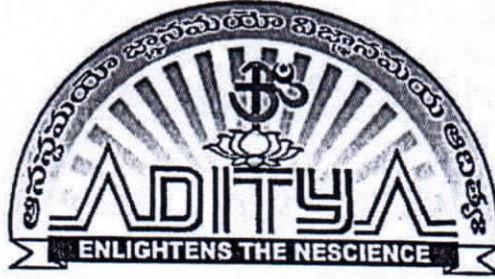
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This is to certify that the Distribution Power Flow Analysis project report entitled "Newton-Downhill Algorithm for Analysis" being submitted by


P. Vamsi Krishna Reddy 13P31A0285

S. Manikanta 13P31A0292

Deepak Kumar 13P31A0261

A J Satya Manoj 11P31A0261

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Associate Professor


Signature of HOD

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CERTIFICATE



This is to certify that the project work entitled "INTELLIGENT ISLANDING AND SEAMLESS RECONNECTION TECHNIQUE FOR MICRO GRID WITH UPQC", is a being submitted by

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V. JAYA KRISHNA 14P35A0215

T.RAVI 13P31A0248

R.NARESH 13P31A0244

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**Aditya College of
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CERTIFICATE

This is to certify that the project work entitled "GRID VOLTAGE SYNCHRONIZATION FOR DISTRIBUTED GENERATION SYSTEMS UNDER GRID FAULT CONDITIONS", is a being submitted by

M.N.SATYA PRASAD	(13P31A0273)
SK.NAZEERUDDIN	(13P31A0290)
M.AVINASH KUMAR	(13P31A0253)
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This is to certify the thesis entitled “ OPERATION OF THREE - PHASE
POWER CONVERTER CONNECTED TO A DISTRIBUTED SYSTEM” is
being submitted by

D. Joshua Victor (13P31A0262)

G. Satish Kumar (13P31A0265)

K. Lahari Sai Monica Priya (13P31A0270)

P. Srinivasa Chowdary (13P31A0284)

Submitted in partial fulfillment of the requirements for the award of Bachelor of
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2013-2017

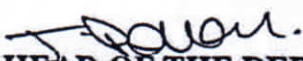
The results embodied in this project have not been submitted to any other
university or institute for the award of any degree or diploma


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Assistant Professor

Dept. of Electrical and Electronics Engineering


HEAD OF THE DEPARTMENT

Mr. J.PAVAN, M. Tech (Ph. D)

Associate Professor

Dept. of Electrical and Electronics Engineering

EXTERNAL EXAMINAR

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ADITYA COLLEGE OF ENGINEERING AND TECHNOLOGY

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Aditya College of
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SURAMPalem- 533 437

CERTIFICATE



This is to certify that the project work entitled “ENERGY AUDITING IN ADITYA EDUCATIONAL INSTITUTIONS”, is a being submitted by

K. MOUNIKA	(13P31A0225)
G. PRASAD	(13P31A0218)
P. VEERA BABU	(14P51A0210)
R. NAGENDRA BABU	(14P35A0211)

in partial fulfillment of the requirements for the award of Bachelor of Technology degree in Electrical & Electronics Engineering, during the academic year 2013-2017. It is record of bonafide work carried out by them under esteemed guidance and supervision of Mr. P. Sridhar, M.Tech


PROJECT GUIDE

Mr. P. Sridhar, M.Tech

Assistant Professor,

Electrical & Electronics Engineering


HEAD OF THE DEPARTMENT

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2013-2017


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CERTIFICATE



This is to certify that the main project report entitled "DESIGN AND ANALYSIS OF ELECTRICAL SOLAR VEHICLE" being submitted by

K. JANAKI DURGA PRASAD	14P35A0208
G. JAMES LEVIS	13P31A0216
B.VENKATA SATYASAI	13P31A0204
CH. SAI RAMA NAGAPRAVEEN	13P31A0211
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Head of the Department

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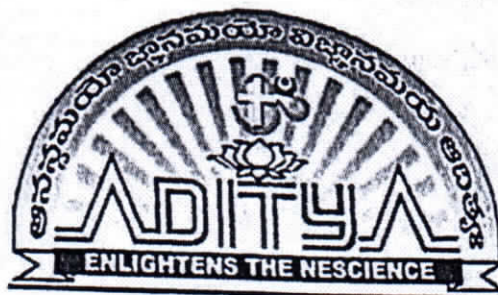
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2013-2017


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CERTIFICATE



This is to certify that the project work entitled "SPACE VECTOR BASED SYNCHRONIZED THREE-LEVEL DISCONTINUOUS PWM FOR MEDIUM-VOLTAGE HIGH-POWER VSP", is a being submitted by

B V S RAGHUNATH SASTRY	(13P31A0208)
N VEERA MANISAI	(13P31A0236)
M SWAMY	(13P31A0234)
S SATISH	(13P31A0245)

Submitted in partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical & Electronics Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.


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Assistant Professor


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CERTIFICATE



This is to certify that the project report entitled **"THREE PHASE FAULT INDICATION WITH AUTOMATIC RESET ON TEMPORARY FAULT AND PERMANENT TRIP WITH MICROCONTROLLER"** being submitted by

B.Sri Hari	14P35A0218
CH.Ramya Susmitha	13P31A0260
K.S.V.A.Naidu	13P31A0271
Rahul Kumar	13P31A0298
E.V.S.A.S.Sasikanth	13P31A0263

in partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL & ELECTRONICS ENGINEERING** is record of bonafied work, carried out by them under esteemed guidance and supervision of **Mr. J. PAVAN**

The results embodied in this project report have not been submitted to any other institute or university for the award of any degree or diploma.


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CERTIFICATE



This is to certify that the report entitled "THE DESIGN AND IMPLEMENTATION OF SOLAR TRACKING GENERATING POWER SYSTEM" being submitted by

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I.SURYA TEJA	14P35A0222
N.MANIKANTA	13P31A0280
O.KUSARAJU	13P31A0282

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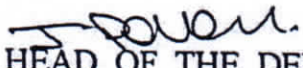
This is to certify that the project report entitled "DESIGN AND IMPLEMENTATION OF 11-LEVEL INVERTER WITH FACTS CAPABILITY FOR DISTRIBUTED ENERGY SYSTEMS" is being submitted by

MANIDEEP RAJ	13P31A0276
G D D TEJASRI	14P35A0221
G KIRAN KUMAR	14P35A0220
P K S KUMAR	13P31A0283

In partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL & ELECTRONICS ENGINEERING** is record of Bonafied work, carried out by them under esteemed guidance and supervision of **Mr. S. REDDY RAMESH**

The results embodied in this project report have not been submitted to any other institute Or university for the award of any degree or diploma.


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Mr. S REDDY RAMESH M. TECH, AMIE.


HEAD OF THE DEPARTMENT
Mr. J PAVAN M. TECH, (PIL D),

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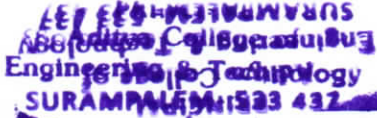
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Aditya Nagar, ADB Road, Surampalem-533437

2013-2017

CERTIFICATE



This is to certify that the project work entitled "A GRID INTERFACING SCHEME FOR RENEWABLE ENERGY RESOURCES", is being submitted by

A.SAIPHALGUNA	13P31A0203
D.SRIHARIREDDY	13P31A0213
P.SUBRAHMANYAM	13P31A0240
SHAIK SARDAR	14P35A0213

Submitted in partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical Engineering, during the academic year 2013-2017.

The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.


PROJECT GUIDE

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Mr. J. PAVAN, M.TECH.(Ph.D)

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Aditya College of
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CERTIFICATE



This is to certify that the main project report entitled “Control of Reduced Rating Dynamic Voltage Restorer with a Battery Energy Storage System” is being submitted by

G. MADHU PHANINDRA SAI	(13P31A0217)
Md.AHAMADI	(14P35A0202)
A.SUDHEER	(14P35A0201)
S.RAHUL SINGH	(13P31A0246)

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Asst professor


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Surampalem, A.D.B.ROAD, East Godavari District

2013-2017


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Aditya College of
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CERTIFICATE



This is to certify that the report entitled **"AN ENHANCED GRID CURRENT COMPENSATOR FOR GRID - CONNECTED DISTRIBUTED GENERATION UNDER NONLINEAR LOADS AND GRID VOLTAGE DISTORTIONS"**

being submitted by

S. NAVEEN KUMAR	13P31A0288
B. MANOHAR REDDY	13P31A0254
G. ANIL	13P31A0266
N. BALAJI PAWAN TANUJ	13P31A0281

in partial fulfillment of the requirements for the award of the degree of **BACHELOR OF TECHNOLOGY** in **ELECTRICAL AND ELECTRONICS ENGINEERING** is record of benefited work, carried out by them under esteemed guidance and supervision of **Mr. M.P.SUBBARAJU, M.Tech**

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2013-2017

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CERTIFICATE



This is to certify that the report entitled "TOUCH SCREEN & ZIGBEE BASED HOME AND INDUSTRIAL AUTOMATION SYSTEM" being submitted by

B.VENKATESWARA RAO	14P35A0219
K.GOPALA KRISHNA	14P35A0223
A.ANUBABU	14P35A0216
MD.AMEER SHARIEF	13P31A0297

in partial fulfillment of the requirements for the award of the degree of BACHELOR OF TECHNOLOGY in ELECTRICAL AND ELECTRONICS ENGINEERING is record of benefited work, carried out by them under esteemed guidance and supervision of Miss.V.U.P.LAVANYA, M.Tech


Project Guide

Miss.V.U.P.LAVANYA, M.Tech, AMIE
Assistant Professor


Signature of HOD

Sri.J.PAVAN, M.Tech(Ph.D), MIE
Associate Professor

Department of Electrical and Electronics Engineering

ADITYA COLLEGE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, Affiliated to JNTUK, Kakinada & Accredited by NAAC)

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Aditya Nagar, ADB Road, Surampalem-533437

2013-2017


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SURAMPALAM- 533 437

CERTIFICATE



This is to certify that the project entitled "A Three-Phase Interleaved Converter with Active Clamp for Fuel Cells" is being submitted by

NUSRAT ANJUM	13P31A0238
M. DURGA SHYAMALA	13P31A0232
K.SATYANARAYANA	13P31A0226
M. VINAY	13P31A0231

in partial fulfillment for the award of degree of B.Tech in Electrical and Electronics Engineering from Jawaharlal Nehru Technological University Kakinada is a record of bonafide work carried out by them at Aditya college of engineering & Technology. The results embodied in this project have not been submitted to any other University or Institute for the award of any degree or diploma.


PROJECT GUIDE

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HEAD OF THE DEPARTMENT

Sri. J. Pavan, M.Tech (Ph.D), MIE
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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
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Surampalem, East Godavari District, A.P, India

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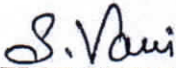


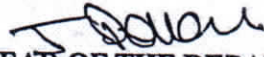
This is to certify that the project work entitled "FAULT CURRENT INTERRUPTION BY THE DYNAMIC VOLTAGE RESTORER" is being submitted by

K. ABBAI REDDY	13P31A0223
J. D L NARAYANA	14P35A0206
D. S L SUDHEER	13P31A0212
M. VENKATESH	13P31A0230

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The results embodied in this Thesis have not been submitted to any other University or Institute for the award of any degree or diploma.


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Mrs. S. Vani, M.Tech,
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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

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TECHNOLOGY**

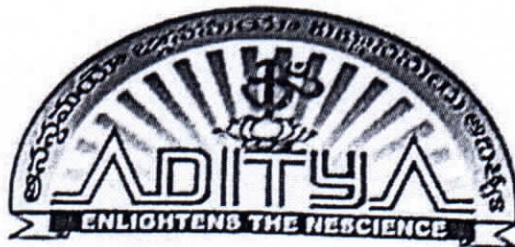
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This is to certify that the report entitled "POWER SYSTEM STATE ESTIMATION USING WEIGHTED LEAST SQUARES TECHNIQUE" being submitted by

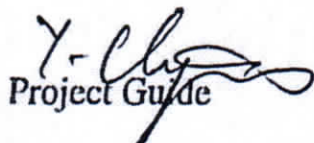
K.NAVEEN 14P35A0224

M.TEJA 14P35A0227

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K.SOMESH 13P31A0269

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Mr.Y.CHIRANJEEVI, M.Tech

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This is to certify that the project work entitled "Improvement of Power Quality by Using Power Conditioner Integrated with Ultra Capacitor", is a being submitted by

V VV S VARA PRASAD (13P31A0294)

R SUDHEER KUMAR (13P31A0287)

K V SURESH KUMAR (14P35A0225)

submitted in partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

K. Vijaya Lakshmi
PROJECT GUIDE

Mrs.K. VIJAYA LAKSHMI, M.TECH
Assistant Professor.

J. Pavan
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This is to certify the thesis entitled "Stability and Voltage Balance Control of A Modular Converter with Multi winding High-Frequency Transformer" is being submitted by

C.SAI TEJA (13P31A0258)

B.M.S.KUMAR (13P31A0257)

B.V.S.SEKHAR (13P31A0255)

M.CHALAPATHI (13P31A0277)

In partial fulfillment of the requirement for the award of the degree of the **Bachelor of Technology in Electrical and Electronics Engineering**. It is record of bonafide work carried out by them under the esteemed guidance and supervision of **Mr.P.SRIDHAR**, M.Tech.

PROJECT GUIDE:

Mr. P. Sridhar, M. Tech, MIE, ISTE

Sr. Assistant Professor

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This is to certify that the project work entitled "SPACE VECTOR BASED SYNCHRONIZED THREE-LEVEL DISCONTINUOUS PWM FOR MEDIUM-VOLTAGE HIGH-POWER VSI", is a being submitted by

K GNANA PRABHA	(13P31A0272)
A N S VINAY	(13P31A0252)
S SNEHA PRIYA	(13P31A0291)
G S S KRISHNA YADAV	(13P31A0264)
K S S S TEJA	(13KA1A0203)

Submitted in partial fulfilment of the requirements for the award of Bachelor of Technology degree in Electrical & Electronics Engineering, during the academic year 2013-2017. The results embodied in this project report have not been submitted to any other institute or university for the award of any degree.

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Assistant Professor

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CERTIFICATE



This is to certify that the main project report entitled "SOLAR POWERED IRRIGATION SYSTEM" Being submitted by

V.KASI	14P35A0229
K.VINEELA	14P35A0226
P.YOSEPU	14P35A0231
S.B.BALAJI	14P35A0230
P.SAI SANDEEP	13KA1A0204

In partial fulfillment of the requirement for the award of the Degree of BACHELOR OF TECHNOLOGY in ELECTRICAL AND ELECTRONICS ENGINEERING is record of bonafide work, carried out by them under the esteemed guidance and supervision of Mr.K.DURGA PRASAD M.Tech.


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Mr. K DURGA PRASAD M.Tech.

Assistant Professor


Head of the Department

Sri J.PAVAN M.Tech.(Ph.D.)

Associate Professor & HOD

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